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HANDBOOK VLŠI Microlithography

Principles, Tools, Technology and Applications - SECOND EDITION



John N. Helbert



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Handbook Of Vlsi Microlithography 2nd Edition Second Edition:

Handbook of VLSI Microlithography, 2nd Edition John N. Helbert, 2001-04 This handbook gives readers a close look at the entire technology of printing very high resolution and high density integrated circuit IC patterns into thin resist process transfer coatings including optical lithography electron beam ion beam and x ray lithography. The book s main theme is the special printing process needed to achieve volume high density IC chip production especially in the Dynamic Random Access Memory DRAM industry The book leads off with a comparison of various lithography methods covering the three major patterning parameters of line space resolution line edge and pattern feature dimension control The book s explanation of resist and resist process equipment technology may well be the first practical description of the relationship between the resist process and equipment parameters. The basics of resist technology are completely covered including an entire chapter on resist process defectivity and the potential yield limiting effect on device production Each alternative lithographic technique and testing method is considered and evaluated basic metrology including optical scanning electron microscope SEM techniques and electrical test devices along with explanations of actual printing tools and their design construction and performance The editor devotes an entire chapter to today's sophisticated complex electron beam printers and to the emerging x ray printing technology now used in high density CMOS devices Energetic ion particle printing is a controllable steerable technology that does not rely on resist and occupies a final section of the handbook Handbook of VLSI Microlithography William B. Glendinning, John N. Helbert, 2012-12-02 This handbook gives readers a close look at the entire technology of printing very high resolution and high density integrated circuit IC patterns into thin resist process transfer coatings including optical lithography electron beam ion beam and x ray lithography The book s main theme is the special printing process needed to achieve volume high density IC chip production especially in the Dynamic Random Access Memory DRAM industry The book leads off with a comparison of various lithography methods covering the three major patterning parameters of line space resolution line edge and pattern feature dimension control The book s explanation of resist and resist process equipment technology may well be the first practical description of the relationship between the resist process and equipment parameters The basics of resist technology are completely covered including an entire chapter on resist process defectivity and the potential yield limiting effect on device production Each alternative lithographic technique and testing method is considered and evaluated basic metrology including optical scanning electron microscope SEM techniques and electrical test devices along with explanations of actual printing tools and their design construction and performance The editor devotes an entire chapter to today's sophisticated complex electron beam printers and to the emerging x ray printing technology now used in high density CMOS devices Energetic ion particle printing is a controllable steerable technology that does not rely on resist and occupies a final section of the handbook *Ultrananocrystalline* Diamond Olga A. Shenderova, Dieter M. Gruen, 2006-08-10 Ultrananocrystalline Diamond Syntheses Properties and

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Semiconductor Manufacturing Technology Chue San Yoo,2008. This textbook contains all the materials that an engineer needs to know to start a career in the semiconductor industry. It also provides readers with essential background information for semiconductor research. It is written by a professional who has been working in the field for over two decades and teaching the material to university students for the past 15 years. It includes process knowledge from raw material preparation to the passivation of chips in a modular format

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