



Figure 2 – Logic Symbol of Half-Subtractor

Inputs		Outputs	
Minuend <i>A</i>	Subtrahend <i>B</i>	Difference	Borrow
0	0	0	0
0	1	1	1
1	0	1	0
1	1	0	0
<i>A – B</i>		<i>Di</i>	<i>Bo</i>

Table 2 – Truth Table of Half-Subtractor

# Half Subtractor Manual Using Cmos

**Dr. Rajiv Chopra**



## Half Subtractor Manual Using Cmos:

*Lab. Manual for CSE/CSE-DS/ AIML/AIDS students-A Practical Manual* Dr. Rajiv Chopra,2025-02-21 Lab Manual for CSE CSE DS AIML AIDS Students By Dr Rajiv Chopra This book serves as a comprehensive lab manual for B Tech students specializing in Computer Science Data Science Artificial Intelligence and Machine Learning Designed with a practical and experiment based approach it bridges the gap between theory and real world application Covering essential programming concepts AI ML techniques and hands on exercises this manual equips students with the skills needed for modern computing challenges Ideal for CSE IT ECE and related disciplines this book encourages students to explore experiment and apply their knowledge effectively in labs and projects *Experiments Manual for Digital Electronics* Roger L. Tokheim,2003 *Digital Electronics Through Project Analysis* Ronald A. Reis,1991 An introductory text to digital circuits for beginning electronics students which provides coverage of basic digital concepts and includes 46 actual digital projects that illustrate concrete applications Coverage encompasses digital combinational and sequential logic circuits Digital Design and Computer Architecture David Harris,Sarah Harris,2010-07-26 Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages HDLs Featuring examples of the two most widely used HDLs VHDL and Verilog the first half of the text prepares the reader for what follows in the second the design of a MIPS Processor By the end of Digital Design and Computer Architecture readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set MIPS Side by side examples of the two most prominent Hardware Design Languages VHDL and Verilog illustrate and compare the ways the each can be used in the design of digital systems Worked examples conclude each section to enhance the reader s understanding and retention of the material Modern Electronic Devices Milton Rosenstein,Paul Morris,1985 *Electronics* ,1988 **Microprocessors and Microcomputer Systems** Guthikonda V. Rao,1978 Covers Theoretical Aspects of the Silicon Semi Conductor Atom as Well as Hardware Software Firmware Applications *Proceedings of the ... Midwest Symposium on Circuits and Systems* ,2000 *Proceedings of Technical Papers* ,2005

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