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Siddappa N.Byrareddy



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HC11 - M68HC11 Motorola,1991 Programming and Customizing the HC11 Microcontroller Thomas Fox,2000
Motorola s 68HC11 microcontrollers are used in appliances ranging from the car to the microwave This text contains experiments and 12 complete projects that demonstrate various applications of the 68HC11 The CD ROM contains all the necessary software to begin developing customized applications **Microcontroller Technology, the 68HC11** Peter Spasov,2002 This updated edition continues to provide readers with the background needed to understand and use microcontrollers specifically the popular Motorola 68HC11 The 68HC11 is relatively easy to work with and has most of the features essential for a complete control system The book starts at an introductory level by explaining the applications and origins of microcontrollers Next a programmer s view of the device is developed Finally the hardware is described and the reader learns how to connect it to the outside world for control applications Many changes have been made to this edition To acknowledge the prominence of C programming the topic is introduced earlier and the text uses C program examples throughout A CD ROM containing source code a special demo version of the THRSim11 simulator a IC11 demo C compiler a cross assembler fuzzy logic tools and assorted electronic design tools is included Because it provides a practical way to explore programming and interfacing concepts readers will find the simulator extremely useful Chapter openers now list learning objectives to help the reader pick out the important points in each chapter Numerous helpful appendices have been added to reinforce key topics This book is an excellent guide and reference and it will prove indispensable to students of control automation and interested amateurs as well as to experienced users of microcontrollers An Instructor s Manual ISBN 0 13 033248 8 is available free of charge to instructors using the book for a course **Whitaker's Books in Print** ,1990

Proceedings of the ... National Conference on Undergraduate Research ,1996 **Electronic Design** ,1996

Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation Vassilis Paliouras,2005-09-06 This book constitutes the refereed proceedings of the 15th International Workshop on Power and Timing Optimization and Simulation PATMOS 2005 held in Leuven Belgium in September 2005 The 74 revised full papers presented were carefully reviewed and selected from numerous submissions The papers are organized in topical sections on low power processors code optimization for low power high level design telecommunications and signal processing low power circuits system on chip design busses and interconnections modeling design automation low power techniques memory and register files applications digital circuits and analog and physical design **HC11 M68HC11 E Series Programming Reference Guide** Motorola, Inc,1993 **Data Acquisition and Process Control with the M68HC11 Microcontroller** Frederick F. Driscoll,Robert F. Coughlin,Robert S. Villanucci,1994 A guide to designing practical embedded controller systems with the Motorola M68HC11 microcontroller An explanation of the workings of the M68HC11 along with the design and development tools needed to create a microcontroller based product that can solve applications

problems is provided *Proceedings of the 14th International Modal Analysis Conference* ,1996 *Smart Structures and Materials* ,1996 *Circuit Cellar Ink* ,1998 **Whitaker's Book List** ,1989 **Robot Science & Technology** ,1998

Proceedings TENCON '93 Baozong Yuan,1993 *CEMS* Geoffrey L. Hausheer,1997 **Realisierung eines Signalprozessorsystems mit 2 Prozessoren und integrierter Entwicklungsumgebung** Markus Bäckmann,1999-09-03

Inhaltsangabe Zusammenfassung Aufgabe dieser Diplomarbeit war es eine Entwicklungsumgebung für einen digitalen Signalprozessor zu erstellen Verwendung findet dabei ein DSP56002 von Motorola mit 40MHz Taktfrequenz 20 MIPS das Entwicklungssystem wird mit Hilfe einer RS232 Schnittstelle mit einem PC verbunden Die Software auf dem PC wurde unter dem Betriebssystem Windows implementiert Das Einsatzgebiet des Systems liegt vor allem in der Lehre es soll die Möglichkeit bieten mit möglichst wenig Schritten einfache Programme für einen DSP zu erstellen Um allerdings auch für komplexere Anwendungen genug Möglichkeiten anzubieten wird großer Wert auf die flexiblen Erweiterungsmöglichkeiten gelegt In der einfachsten Ausbaustufe wird ein DSP56002 im Single Chip Betrieb also ohne externen Speicher unterstützt in der Maximalausbaustufe eine Erweiterung auf 192Kx24 Bit Worte statisches RAM und Unterstützung eines zweiten DSP56002 der als echter Parallelprozessor arbeiten kann Die Aufstellung orientiert sich am Stand der sogenannten Plug Play Technik d.h. die Erweiterungen werden eingesetzt und automatisch erkannt Die Ausbaustufe mit einem Signalprozessor sollte nahtlos in den Betrieb mit einem zweiten DSP übergehen d.h. die Software bietet für beide DSPs adäquate Debugging und Entwicklungsmöglichkeiten Funktionsumfang Die DSPs sollen mit der vollen Arbeitsgeschwindigkeit von 40MHz arbeiten als externer Basistakt wird ein 1MHz Takt eingesetzt der durch die interne PLL multipliziert wird Dadurch ist eine Systemerweiterung auf den DSP56002 mit 66MHz prinzipiell denkbar Der Speicherzugriff soll mit voller Geschwindigkeit also 0 Waitstates erfolgen Der Speicher steht beiden Signalprozessoren gleichermaßen zur Verfügung eine Busüberwachung verteilt den globalen Bus auf die DSPs Die Kommunikation und Synchronisation der beiden Parallel DSPs kann über den gemeinsamen Speicher Semaphoren oder über Interruptsignale erfolgen Um dem DSP System eine Anbindung an die Außenwelt zu ermöglichen wird ein flexibler Port zum Anschluß von AD/DA Umsetzern bereitgestellt sowie eine zusätzliche serielle RS232 Schnittstelle für den DSP 1 Der Port zum Anschluß von Analog Einheiten bietet eine volle 24 Bit Busanbindung unterstützt ADUs und DAUs auch in Mehrkanalausführung ist für beide DSPs nutzbar und die maximale Abtastfrequenz wird nur durch den Systemtakt der DSPs begrenzt theoretisch 10MHz Durch die zusätzliche **MicroComputer Journal** ,1996

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