

# Low-Voltage, Low-Power VLSI Subsystems

- ✓ New approaches to silicon-level design for low-power circuitry pacing electronics and ICs
- ✓ Low-power solutions for adders, multipliers, ROMs, SRAMs, DRAMs, and signal processors

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Kiat-Seng Yeo  
Kaushik Roy

# Low Voltage Low Power Vlsi Subsystems

**Rajesh Singh, Anita Gehlot**



## **Low Voltage Low Power Vlsi Subsystems:**

*Low Voltage, Low Power VLSI Subsystems* Kiat Seng Yeo, Kaushik Roy, 2005 Designers developing the low voltage low power chips that enable small portable devices face a very particular set of challenges This monograph details design techniques for the low power circuitry required by the many miniaturized business and consumer products driving the electronics market *CMOS VLSI Subsystems for Low-voltage Low-power Applications* Zhi Hui Kong, 2006 **Low Power VLSI Circuits and Systems** Mr. Rohit Manglik, 2024-04-06 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels Low Power Methodology Manual David Flynn, Rob Aitken, Alan Gibbons, Kaijian Shi, 2007-07-31 Tools alone aren't enough to reduce dynamic and leakage power in complex chip designs a well planned methodology is needed Following in the footsteps of the successful Reuse Methodology Manual RMM authors from ARM and Synopsys have written this Low Power Methodology Manual LPMM to describe such a low power methodology with a practical step by step approach Richard Goering Software Editor EE Times Excellent compendium of low power techniques and guidelines with balanced content spanning theory and practical implementation The LPMM is a very welcome addition to the field of low power SoC implementation that has for many years operated in a largely ad hoc fashion Sujeeth Joseph Chief Architect Semiconductor and Systems Solutions Unit Wipro Technologies The LPMM enables broader adoption of aggressive power management techniques based on extensive experience and silicon example with real data that every SOC designer can use to meet the difficulties faced in managing the power issues in deep submicron designs Anil Mankar Sr VP Worldwide Core Engineering and Chief Development Officer Conexant Systems Inc Managing power at 90nm and below introduces significant challenges to design flow The LPMM is a timely and immediately useful book that shows how combination of tools IP and methodology can be used together to address power management Nick Salter Head of Chip Integration CSR plc

**Low-Power High-Level Synthesis for Nanoscale CMOS Circuits** Saraju P. Mohanty, Nagarajan Ranganathan, Elias Kougianos, Priyadarsan Patra, 2008-05-31 Low Power High Level Synthesis for Nanoscale CMOS Circuits addresses the need for analysis characterization estimation and optimization of the various forms of power dissipation in the presence of process variations of nano CMOS technologies The authors show very large scale integration VLSI researchers and engineers how to minimize the different types of power consumption of digital circuits The material deals primarily with high level architectural or behavioral energy dissipation because the behavioral level is not as highly abstracted as the system level nor is it as complex as the gate transistor level At the behavioral level there is a balanced degree of freedom to explore power reduction mechanisms the power reduction opportunities are greater and it can cost effectively help in investigating lower power design alternatives prior to actual circuit layout or silicon implementation The book is a self contained low power high

level synthesis text for Nanoscale VLSI design engineers and researchers Each chapter has simple relevant examples for a better grasp of the principles presented Several algorithms are given to provide a better understanding of the underlying concepts The initial chapters deal with the basics of high level synthesis power dissipation mechanisms and power estimation In subsequent parts of the text a detailed discussion of methodologies for the reduction of different types of power is presented including Power Reduction Fundamentals Energy or Average Power Reduction Peak Power Reduction Transient Power Reduction Leakage Power Reduction Low Power High Level Synthesis for Nanoscale CMOS Circuits provides a valuable resource for the design of low power CMOS circuits *Low Power RF Circuit Design in Standard CMOS*

*Technology* Unai Alvarado,Guillermo Bistu ,I igo Ad n,2011-10-18 Low Power Consumption is one of the critical issues in the performance of small battery powered handheld devices Mobile terminals feature an ever increasing number of wireless communication alternatives including GPS Bluetooth GSM 3G WiFi or DVB H Considering that the total power available for each terminal is limited by the relatively slow increase in battery performance expected in the near future the need for efficient circuits is now critical This book presents the basic techniques available to design low power RF CMOS analogue circuits It gives circuit designers a complete guide of alternatives to optimize power consumption and explains the application of these rules in the most common RF building blocks LNA mixers and PLLs It is set out using practical examples and offers a unique perspective as it targets designers working within the standard CMOS process and all the limitations inherent in these technologies **Power-Aware Testing and Test Strategies for Low Power Devices** Patrick

Girard,Nicola Nicolici,Xiaoqing Wen,2010-03-11 Managing the power consumption of circuits and systems is now considered one of the most important challenges for the semiconductor industry Elaborate power management strategies such as dynamic voltage scaling clock gating or power gating techniques are used today to control the power dissipation during functional operation The usage of these strategies has various implications on manufacturing test and power aware test is therefore increasingly becoming a major consideration during design for test and test preparation for low power devices This book explores existing solutions for power aware test and design for test of conventional circuits and systems and surveys test strategies and EDA solutions for testing low power devices Energy-Efficient Fault-Tolerant Systems Jimson

Mathew,Rishad A. Shafik,Dhiraj K. Pradhan,2013-09-07 This book describes the state of the art in energy efficient fault tolerant embedded systems It covers the entire product lifecycle of electronic systems design analysis and testing and includes discussion of both circuit and system level approaches Readers will be enabled to meet the conflicting design objectives of energy efficiency and fault tolerance for reliability given the up to date techniques presented **Low-Power Digital VLSI Design** Abdellatif Bellaouar,Mohamed Elmasry,2012-12-06 Low Power Digital VLSI Design Circuits and Systems addresses both process technologies and device modeling Power dissipation in CMOS circuits several practical circuit examples and low power techniques are discussed Low voltage issues for digital CMOS and BiCMOS circuits are

emphasized The book also provides an extensive study of advanced CMOS subsystem design A low power design methodology is presented with various power minimization techniques at the circuit logic architecture and algorithm levels Features Low voltage CMOS device modeling technology files design rules Switching activity concept low power guidelines to engineering practice Pass transistor logic families Power dissipation of I/O circuits Multi and low VT CMOS logic static power reduction circuit techniques State of the art design of low voltage BiCMOS and CMOS circuits Low power techniques in CMOS SRAMS and DRAMS Low power on chip voltage down converter design Numerous advanced CMOS subsystems e.g. adders multipliers data path memories regular structures phase locked loops with several design options trading power delay and area Low power design methodology power estimation techniques Power reduction techniques at the logic architecture and algorithm levels More than 190 circuits explained at the transistor level

**Low Power VLSI Design and Technology**  
 Gary K. Yeap, Farid N. Najm, 1996 Low power and low energy VLSI has become an important issue in today's consumer electronics This book is a collection of pioneering applied research papers in low power VLSI design and technology A comprehensive introductory chapter presents the current status of the industry and academic research in the area of low power VLSI design and technology Other topics cover logic synthesis floorplanning circuit design and analysis from the perspective of low power requirements The readers will have a sampling of some key problems in this area as the low power solutions span the entire spectrum of the design process The book also provides excellent references on up to date research and development issues with practical solution techniques

*Low-Power Variation-Tolerant Design in Nanometer Silicon*  
 Swarup Bhunia, Saibal Mukhopadhyay, 2010-11-10 Design considerations for low power operations and robustness with respect to variations typically impose contradictory requirements Low power design techniques such as voltage scaling dual threshold assignment and gate sizing can have large negative impact on parametric yield under process variations This book focuses on circuit architectural design techniques for achieving low power operation under parameter variations We consider both logic and memory design aspects and cover modeling and analysis as well as design methodology to achieve simultaneously low power and variation tolerance while minimizing design overhead This book will discuss current industrial practices and emerging challenges at future technology nodes

Semiconductor Devices and Technologies for Future Ultra Low Power Electronics  
 D. Nirmal, J. Ajayan, Patrick J. Fay, 2021-12-09 This book covers the fundamentals and significance of 2D materials and related semiconductor transistor technologies for the next generation ultra low power applications It provides comprehensive coverage on advanced low power transistors such as NCFETs FinFETs TFETs and flexible transistors for future ultra low power applications owing to their better subthreshold swing and scalability In addition the text examines the use of field effect transistors for biosensing applications and covers design considerations and compact modeling of advanced low power transistors such as NCFETs FinFETs and TFETs TCAD simulation examples are also provided FEATURES Discusses the latest updates in the field of ultra low power semiconductor transistors Provides both

experimental and analytical solutions for TFETs and NCFETs Presents synthesis and fabrication processes for FinFETs Reviews details on 2 D materials and 2 D transistors Explores the application of FETs for biosensing in the healthcare field This book is aimed at researchers professionals and graduate students in electrical engineering electronics and communication engineering electron devices nanoelectronics and nanotechnology microelectronics and solid state circuits

*Top-Down Digital VLSI Design* Hubert Kaeslin,2014-12-07 Top Down VLSI Design From Architectures to Gate Level Circuits and FPGAs represents a unique approach to learning digital design Developed from more than 20 years teaching circuit design Doctor Kaeslin s approach follows the natural VLSI design flow and makes circuit design accessible for professionals with a background in systems engineering or digital signal processing It begins with hardware architecture and promotes a system level view first considering the type of intended application and letting that guide your design choices Doctor Kaeslin presents modern considerations for handling circuit complexity throughput and energy efficiency while preserving functionality The book focuses on application specific integrated circuits ASICs which along with FPGAs are increasingly used to develop products with applications in telecommunications IT security biomedical automotive and computer vision industries Topics include field programmable logic algorithms verification modeling hardware synchronous clocking and more Demonstrates a top down approach to digital VLSI design Provides a systematic overview of architecture optimization techniques Features a chapter on field programmable logic devices their technologies and architectures Includes checklists hints and warnings for various design situations Emphasizes design flows that do not overlook important action items and which include alternative options when planning the development of microelectronic circuits **Advances**

**in Power Systems and Energy Management** Amik Garg,Akash Kumar Bhoi,Padmanaban Sanjeevikumar,K. K. Kamani,2017-11-28 This book is a collection of research articles and critical review articles describing the overall approach to energy management The book emphasizes the technical issues that drive energy efficiency in context of power systems This book contains case studies with and without solutions on modelling simulation and optimization techniques It covers some innovative topics such as medium voltage MV back to back BTB system cost optimization of a ring frame unit in textile industry rectenna for radio frequency RF energy harvesting ecology and energy dimension in infrastructural designs 2 4 kW three phase inverter for aircraft application study of automatic generation control AGC in a two area hydrothermal power system energy efficient and reliable depth based routing protocol for underwater wireless sensor network and power line communication using LabVIEW This book is primarily targeted at researchers and senior graduate students but is also highly useful for the industry professional and scientists **Intellectual Property for Integrated Circuits** Kiat Seng Yeo,Kim Tean Ng,Zhi Hui Kong,Tricia Bee Yoke Dang,2010 Intellectual Property for Integrated Circuits provides inventors with the know how to effectively search for and interpret prior arts and equips them with the knowledge to be granted exclusive rights to control the results of their creativity and to benefit financially from those rights Low-Power VLSI Circuits and

Systems Ajit Pal,2014-11-17 The book provides a comprehensive coverage of different aspects of low power circuit synthesis at various levels of design hierarchy starting from the layout level to the system level For a seamless understanding of the subject basics of MOS circuits has been introduced at transistor gate and circuit level followed by various low power design methodologies such as supply voltage scaling switched capacitance minimization techniques and leakage power minimization approaches The content of this book will prove useful to students researchers as well as practicing engineers **Process**

**Variations and Probabilistic Integrated Circuit Design** Manfred Dietrich,Joachim Haase,2011-11-20 Uncertainty in key parameters within a chip and between different chips in the deep sub micron area plays a more and more important role As a result manufacturing process spreads need to be considered during the design process Quantitative methodology is needed to ensure faultless functionality despite existing process variations within given bounds during product development This book presents the technological physical and mathematical fundamentals for a design paradigm shift from a deterministic process to a probability orientated design process for microelectronic circuits Readers will learn to evaluate the different sources of variations in the design flow in order to establish different design variants while applying appropriate methods and tools to evaluate and optimize their design **Digital Integrated Circuit Design** Hubert Kaeslin,2008-04-28 This

practical tool independent guide to designing digital circuits takes a unique top down approach reflecting the nature of the design process in industry Starting with architecture design the book comprehensively explains the why and how of digital circuit design using the physics designers need to know and no more **Wearable and Neuronic Antennas for Medical**

**and Wireless Applications** Arun Kumar,Manoj Gupta,Mahmoud A. Albreem,Dac-Binh Ha,Er. Mohit Kumar

Sharma,2022-04-29 WEARABLE AND NEURONIC ANTENNAS FOR MEDICAL AND WIRELESS APPLICATIONS This new volume in this exciting new series written and edited by a group of international experts in the field covers the latest advances and challenges in wearable and neuronic antennas for medical and wireless applications Antenna development and engineering is changing at a rapid pace and it is incredibly important that engineers scientists and students in the field have a valuable reference work to consult Students are able to use this book as a learning tool and professors and industrial short courses are able to use it as a textbook Covering all of the advances and developments of wearable and neuronic antennas for medical and wireless applications this outstanding new volume offers information not available anywhere else in any other format Covering new research and development of antenna designs never seen before this volume written and edited by a team of experts in the field breaks new ground offering new solutions to engineering and scientific problems to experts in the field while providing the full theoretical and conceptual background for the practical applications Whether for the veteran engineer or scientist the student or a manager or other technician working in the field this volume is a must have for any library **Emerging Electronics and Automation** Daniel-Ioan Stroe,Nasimuddin,Shahedul Haque Laskar,Shivendra

Kumar Pandey,2025-02-12 This book includes peer reviewed papers presented at the 3rd International Conference on

Emerging Electronics Automation E2A 2023 This volume will serve as a comprehensive compilation of the scientific exchanges that took place during the conference at NIT Silchar India These proceedings aim to provide readers with insights into the latest scientific endeavors and accomplishments of the conference participants in various emerging fields including instrumentation control signal processing communication and related computational techniques The book seeks to present the global audience with exciting updates novel findings and solutions to challenging questions in the field while also inspiring aspiring scientists to pursue meaningful scientific research Despite its specialization the field of instrumentation spans a wide range of disciplines such as electronics computation automation microelectronic technology nanomaterials and biomedical engineering applications Consequently this publication is expected to appeal to a diverse audience within the scientific and engineering domains

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