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IA-64 and Elementary Functions Peter Markstein, 2000 PLEASE PROVIDE COURSE INFORMATION PLEASE PROVIDE *Elementary Functions* Jean-Michel Muller, 2006-09-11 An important topic which is on the boundary between numerical analysis and computer science I found the book well written and containing much interesting material most of the time disseminated in specialized papers published in specialized journals difficult to find Moreover there are very few books on these topics and they are not recent Numerical Algorithms review of the first edition This unique book provides concepts and background necessary to understand and build algorithms for computing the elementary functions The author presents and structures the algorithms hardware oriented as well as software oriented and also discusses issues related to accurate floating point implementation The purpose is not to give cookbook recipes that allow one to implement a given function but rather to provide the reader with tools necessary to build or adapt algorithms to their specific computing environment The expanded second edition contains a number of revisions and additions which incorporate numerous new results obtained during the last few years New algorithms invented since 1997 such as Matula's bipartite method another table based method due to Ercegovic Lang Tisserand and Muller as well as a new chapter on multiple precision arithmetic have been added In addition the section on correct rounding of elementary functions has been fully reworked also in the context of new results Finally the introductory presentation of floating point arithmetic has been expanded with more emphasis given to the use of the fused multiply accumulate instruction The book is an up to date presentation of information needed to understand and accurately use mathematical functions and algorithms in computational work and design Graduates professionals and researchers in scientific computing software engineering and computer engineering will find the book a useful reference and resource **Handbook of Floating-Point Arithmetic** Jean-Michel Muller, Nicolas Brisebarre, Florent de Dinechin, Claude-Pierre Jeannerod, Vincent Lefèvre, Guillaume Melquiond, Nathalie Revol, Damien Stehlé, Serge Torres, 2009-11-11 Floating point arithmetic is the most widely used way of implementing real number arithmetic on modern computers However making such an arithmetic reliable and portable yet fast is a very difficult task As a result floating point arithmetic is far from being exploited to its full potential This handbook aims to provide a complete overview of modern floating point arithmetic So that the techniques presented can be put directly into practice in actual coding or design they are illustrated whenever possible by a corresponding program The handbook is designed for programmers of numerical applications compiler designers programmers of floating point algorithms designers of arithmetic operators and more generally students and researchers in numerical analysis who wish to better understand a tool used in their daily work and research **Numerical Validation in Current Hardware Architectures** Annie A.M. Cuyt, Walter Krämer, Wolfram Luther, Peter Markstein, 2009-04-24 The major emphasis of the Dagstuhl Seminar on Numerical Validation in Current Hardware Architectures lay on numerical validation in current hardware architectures and software environments The

general idea was to bring together experts who are concerned with computer arithmetic in systems with actual processor architectures and scientists who develop use and need techniques from verified computation in their applications. Topics of the seminar therefore included: The ongoing revision of the IEEE 754-854 standard for floating point arithmetic; Feasible ways to implement multiple precision multiword arithmetic and to compute the actual precision at run time according to the needs of input data; The achievement of a similar behavior of fixed point floating point and interval arithmetic across language compliant implementations; The design of robust and efficient numerical programs portable from diverse computers to those that adhere to the IEEE standard; The development and propagation of validated special purpose software in different application areas; Error analysis in several contexts; Certification of numerical programs; verification and validation assessment; Computer arithmetic plays an important role at the hardware and software level when microprocessors, embedded systems or grids are designed; The reliability of numerical software strongly depends on the compliance with the corresponding floating point norms; Standard CISC processors follow the 1985 IEEE norm 754 which is currently under revision but the new highly performing CELL processor is not fully IEEE compliant.

Application-Specific Arithmetic Florent de Dinechin, Martin Kumm, 2024-03-12

Written by two experts of the domain, this book presents the most recent advances in computer arithmetic hardware with a focus on application specific arithmetic beyond the classic operators and the standard precisions. It targets silicon designers who have to do better with less in the post Moore era and FPGA developers who want to exploit the full possibilities of reconfigurable computing platforms.

Digital Arithmetic Milos D. Ercegovac, Tomás Lang, 2004

The authoritative reference on the theory and design practice of computer arithmetic.

Computer Aided Verification Ruzica Piskac, Zvonimir Rakamarić, 2025-07-21

This open access 4 volume set constitutes the proceedings of the 37th International Conference on Computer Aided Verification CAV 2025 held in Zagreb, Croatia in July 23-25, 2025. The 51 regular papers presented together 24 tool papers, 4 case study papers in these proceedings were carefully reviewed and selected from 305 submissions. The accepted papers cover a wide spectrum of topics from theoretical results to applications of formal methods. These papers apply or extend formal methods to a wide range of domains such as concurrency, machine learning and neural networks, quantum systems as well as hybrid and stochastic systems.

2007 International Conference on Parallel Processing IEEE Staff, 2007

American Book Publishing Record R.R. Bowker Company, 1978

The British National Bibliography Arthur James Wells, 2000

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