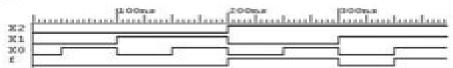
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#### 2-42.



#### 2-43."

module circuit A = 53(X, Y, Z, B) input X, Y, Z; output F; assign  $F = (X \& Z) \mid (Z \& -Y)$ ; endraodule

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# **Logic Instructors Solution Manual**

**Jin-Ying Zhang** 

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