



Other Loop Restructuring Transformations

- *Loop skewing*: denormalize iteration vectors to change the shape of the iteration space (skew) to allow loop interchange
- *Strip mining*: decompose a single loop into two nested loops (where the inner loop computes a strip of the data)
- *Loop tiling*: the loop space is divided into tiles

Loop Transformations For Restructuring Compilers

Loop Transformations For Restructuring Compilers

Chao Zhang



Loop Transformations For Restructuring Compilers Loop Transformations For Restructuring Compilers:

Loop Transformations for Restructuring Compilers Utpal Banerjee, 2007-07-07 Automatic transformation of a sequential program into a parallel form is a subject that presents a great intellectual challenge and promises great practical rewards There is a tremendous investment in existing sequential programs and scientists and engineers continue to write their application programs in sequential languages primarily in Fortran but the demand for increasing speed is constant The job of a restructuring compiler is to discover the dependence structure of a given program and transform the program in a way that is consistent with both that dependence structure and the characteristics of the given machine Much attention in this field of research has been focused on the Fortran do loop This is where one expects to find major chunks of computation that need to be performed repeatedly for different values of the index variable Many loop transformations have been designed over the years and several of them can be found in any parallelizing compiler currently in use in industry or at a university research facility **Loop Transformations for Restructuring Compilers The Foundations** provides a rigorous theory of loop transformations The transformations are developed in a consistent mathematical framework using objects like directed graphs matrices and linear equations The algorithms that implement the transformations can then be precisely described in terms of certain abstract mathematical algorithms The book provides the general mathematical background needed for loop transformations including those basic mathematical algorithms discusses data dependence and introduces the major transformations The next volume will build a detailed theory of loop transformations based on the material developed here **Loop Transformations for Restructuring Compilers The Foundations** presents a theory of loop transformations that is rigorous and yet reader friendly *Loop Transformations for Restructuring Compilers* Utpal Banerjee, 2014-01-15 **A**

book series on loop transformations for restructuring compilers [Anonymus AC04353067], 1993 **Loop**

Parallelization Utpal Banerjee, 1994-04-30 We present a theory of loop transformations that is rigorous and yet reader friendly this will make it easier to learn the subject and do research in this area *Loop Parallelization* Utpal Banerjee, 2013-06-29 Automatic transformation of a sequential program into a parallel form is a subject that presents a great intellectual challenge and promises a great practical award There is a tremendous investment in existing sequential programs and scientists and engineers continue to write their application programs in sequential languages primarily in Fortran The demand for higher speedups increases The job of a restructuring compiler is to discover the dependence structure and the characteristics of the given machine Much attention has been focused on the Fortran do loop This is where one expects to find major chunks of computation that need to be performed repeatedly for different values of the index variable Many loop transformations have been designed over the years and several of them can be found in any parallelizing compiler currently in use in industry or at a university research facility The book series on KappaLoop Transformations for Restructuring Compilers kappa provides a rigorous theory of loop transformations and dependence analysis We want to

develop the transformations in a consistent mathematical framework using objects like directed graphs matrices and linear equations Then the algorithms that implement the transformations can be precisely described in terms of certain abstract mathematical algorithms The first volume Loop Transformations for Restructuring Compilers The Foundations provided the general mathematical background needed for loop transformations including those basic mathematical algorithms discussed data dependence and introduced the major transformations The current volume Loop Parallelization builds a detailed theory of iteration level loop transformations based on the material developed in the previous book

Parallel Processing and Applied Mathematics Roman Wyrzykowski,Jack Dongarra,Ewa Deelman,Konrad Karczewski,2018-03-22 The two volume set LNCS 10777 and 10778 constitutes revised selected papers from the 12th International Conference on Parallel Processing and Applied Mathematics PPAM 2017 held in Lublin Poland in September 2017 The 49 regular papers presented in this volume were selected from 98 submissions For the workshops and special sessions that were held as integral parts of the PPAM 2017 conference a total of 51 papers was accepted from 75 submissions The papers were organized in topical sections named as follows Part I numerical algorithms and parallel scientific computing particle methods in simulations task based paradigm of parallel computing GPU computing parallel non numerical algorithms performance evaluation of parallel algorithms and applications environments and frameworks for parallel distributed cloud computing applications of parallel computing soft computing with applications and special session on parallel matrix factorizations Part II workshop on models algorithms and methodologies for hybrid parallelism in new HPC systems workshop power and energy aspects of computations PEAC 2017 workshop on scheduling for parallel computing SPC 2017 workshop on language based parallel programming models WLPP 2017 workshop on PGAS programming minisymposium on HPC applications in physical sciences minisymposium on high performance computing interval methods workshop on complex collective systems

Compiler Construction Tibor Gyimothy,1996-04-03 This book presents the refereed proceedings of the Sixth International Conference on Compiler Construction CC 96 held in Linköping Sweden in April 1996 The 23 revised full papers included were selected from a total of 57 submissions also included is an invited paper by William Waite entitled Compiler Construction Craftsmanship or Engineering The book reports the state of the art in the area of theoretical foundations and design of compilers among the topics addressed are program transformation software pipelining compiler optimization program analysis program inference partial evaluation implementational aspects and object oriented compilers

Languages and Compilers for Parallel Computing Keshav Pingali,1995-01-26 This volume presents revised versions of the 32 papers accepted for the Seventh Annual Workshop on Languages and Compilers for Parallel Computing held in Ithaca NY in August 1994 The 32 papers presented report on the leading research activities in languages and compilers for parallel computing and thus reflect the state of the art in the field The volume is organized in sections on fine grain parallelism alignment and distribution postlinear loop transformation parallel structures program analysis computer communication automatic

parallelization languages for parallelism scheduling and program optimization and program evaluation Encyclopedia of Parallel Computing David Padua,2014-07-08 Containing over 300 entries in an A Z format the Encyclopedia of Parallel Computing provides easy intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing Topics for this comprehensive reference were selected written and peer reviewed by an international pool of distinguished researchers in the field The Encyclopedia is broad in scope covering machine organization programming languages algorithms and applications Within each area concepts designs and specific implementations are presented The highly structured essays in this work comprise synonyms a definition and discussion of the topic bibliographies and links to related literature Extensive cross references to other entries within the Encyclopedia support efficient user friendly searchers for immediate access to useful information Key concepts presented in the Encyclopedia of Parallel Computing include laws and metrics specific numerical and non numerical algorithms asynchronous algorithms libraries of subroutines benchmark suites applications sequential consistency and cache coherency machine classes such as clusters shared memory multiprocessors special purpose machines and dataflow machines specific machines such as Cray supercomputers IBM s cell processor and Intel s multicore machines race detection and auto parallelization parallel programming languages synchronization primitives collective operations message passing libraries checkpointing and operating systems Topics covered Speedup Efficiency Isoefficiency Redundancy Amdahls law Computer Architecture Concepts Parallel Machine Designs Benmarks Parallel Programming concepts design Algorithms Parallel applications This authoritative reference will be published in two formats print and online The online edition features hyperlinks to cross references and to additional significant research Related Subjects supercomputing high performance computing distributed computing **Languages and Compilers for Parallel Computing** Lawrence Rauchwerger,2004-05-13 This book constitutes the thoroughly refereed post proceedings of the 16th International Workshop on Languages and Compilers for Parallel Computing LCPC 2003 held in College Station Texas USA in October 2003 The 35 revised full papers presented were selected from 48 submissions during two rounds of reviewing and improvement upon presentation at the workshop The papers are organized in topical sections on adaptive optimization data locality parallel languages high level transformations embedded systems distributed systems software low level transformations compiling for novel architectures and optimization infrastructure Vector and Parallel Processing - VECPAR 2000 Jose M.L.M. Palma,Jack Dongarra,Vicente Hernandez,2003-06-29 This book is the nal outcome of VECPAR 2000 4th International Meeting on Vector and Parallel Processing VECPAR constitutes a series of conferences which have been organized by the Faculty of Engineering of the University of Porto since 1993 with the main objective of disseminating new knowledge on parallel computing Readership of This Book The book is aimed at an audience of researchers and graduate students in a broad range of scienti c areas including not only computer science but also applied mathematics and numerical analysis physics and engineering Book Plan

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Languages and Compilers for Parallel Computing Samuel P. Midkiff, Jose E. Moreira, Manish Gupta, Siddhartha Chatterjee, Jeanne Ferrante, Jan Prins, William Pugh, Chau-Wen Tseng, 2003-06-29 This volume contains the papers presented at the 13th International Workshop on Languages and Compilers for Parallel Computing It also contains extended abstracts of submissions that were accepted as posters The workshop was held at the IBM T J Watson Research Center in Yorktown Heights New York As in previous years the workshop focused on issues in optimizing compilers languages and software environments for high performance computing This continues a trend in which languages compilers and software environments for high performance computing and not strictly parallel computing has been the organizing topic As in past years participants came from Asia North America and Europe This workshop reflected the work of many people In particular the members of the steering committee David Padua Alex Nicolau Utpal Banerjee and David Gelernter have been instrumental in maintaining the focus and quality of the workshop since it was first held in 1988 in Urbana Champaign The assistance of the other members of the program committee Larry Carter Sid Chatterjee Jeanne Ferrante Jan Prins Bill Pugh and Chau wen Tseng was crucial The infrastructure at the IBM T J Watson Research Center provided trouble free logistical support The IBM T J Watson Research Center also provided financial support by underwriting much of the expense of the workshop Appreciation must also be extended to Marc Snir and Pratap Pattnaik of the IBM T J Watson Research Center for their support

Computational Science and Its Applications - ICCSA 2004 Antonio Laganà, Marina L. Gavrilova, Vipin Kumar, Youngsong Mun, C.J. Kenneth Tan, Osvaldo Gervasi, 2004-04-29 The natural mission of Computational Science is to tackle all sorts of human problems and to work out intelligent automata aimed at alleviating the burden of working out suitable tools for solving complex problems For this reason Computational Science though originating from the need to solve the most challenging problems in science and engineering computational science is the key player in the fight to gain fundamental advances in astronomy biology chemistry environmental science physics and several other scientific and engineering disciplines is increasingly turning its attention to all fields of human activity In all activities in fact intensive computation information handling knowledge synthesis the use of ad hoc devices etc increasingly need to be exploited and coordinated regardless of the location of both the users and the various and heterogeneous computing platforms As a result the key to understanding the explosive growth of this discipline lies in two

adjectives that more and more appropriately refer to Computational Science and its applications interoperable and ubiquitous Numerous examples of ubiquitous and interoperable tools and applications are given in the present four LNCS volumes containing the contributions delivered at the 2004 International Conference on Computational Science and its Applications ICCSA 2004 held in Assisi Italy May 14-17 2004 *Euro-Par 2004 Parallel Processing* Marco Danelutto, Domenico Laforenza, Marco Vanneschi, 2004-08-19 This book constitutes the refereed proceedings of the 10th International Conference on Parallel Computing Euro Par 2004 held in Pisa Italy in August September 2004 The 122 revised papers presented together with 3 invited papers were carefully reviewed and selected from 352 submissions The papers are organized in topical sections on support tools and environments performance evaluation scheduling and load balancing compilers and high performance parallel and distributed databases grid and cluster computing applications on high performance clusters parallel computer architecture and ILP distributed systems and algorithms parallel programming numerical algorithms high performance multimedia theory and algorithms for parallel computing routing and communication in interconnection networks mobile computing integrated problem solving environments high performance bioinformatics and peer to peer and Web computing **Compiler Construction** Michael O'Boyle, 2012-03-22 This book constitutes the proceedings of the 21st International Conference on Compiler Construction CC 2012 held as part of the joint European Conference on Theory and Practice of Software ETAPS 2012 which took place in Tallinn Estonia in March April 2012 The 13 papers presented in this book were carefully reviewed and selected from 51 submissions They are organized in topical sections named GPU optimisation program analysis objects and components and dynamic analysis and runtime support *Languages and Compilers for Parallel Computing* Larry Carter, Jeanne Ferrante, 2003-06-29 In August 1999 the Twelfth Workshop on Languages and Compilers for Parallel Computing LCPC was hosted by the Hierarchical Tiling Research group from the Computer Science and Engineering Department at the University of California San Diego UCSD The workshop is an annual international forum for leading research groups to present their current research activities and the latest results It has also been a place for researchers and practitioners to interact closely and exchange ideas about future directions Among the topics of interest to the workshop are language features code generation debugging timization communication and distributed shared memory libraries distributed object systems resource management systems integration of compiler and runtime systems irregular and dynamic applications and performance evaluation In 1999 the workshop was held at the International Relations Pacific Studies Auditorium and the San Diego Supercomputer Center at UCSD Seventy seven researchers from Australia England France Germany Korea Spain and the United States attended the workshop an increase of over 50% from 1998 Information Systems Architecture and Technology: Proceedings of 39th International Conference on Information Systems Architecture and Technology - ISAT 2018 Leszek Borzemski, Jerzy Świątek, Zofia Wilimowska, 2018-08-28 This three volume set of books highlights major advances in

the development of concepts and techniques in the area of new technologies and architectures of contemporary information systems Further it helps readers solve specific research and analytical problems and glean useful knowledge and business value from the data Each chapter provides an analysis of a specific technical problem followed by a numerical analysis simulation and implementation of the solution to the real life problem Managing an organisation especially in today s rapidly changing circumstances is a very complex process Increased competition in the marketplace especially as a result of the massive and successful entry of foreign businesses into domestic markets changes in consumer behaviour and broader access to new technologies and information calls for organisational restructuring and the introduction and modification of management methods using the latest advances in science This situation has prompted many decision making bodies to introduce computer modelling of organisation management systems The three books present the peer reviewed proceedings of the 39th International Conference Information Systems Architecture and Technology ISAT held on September 16 18 2018 in Nysa Poland The conference was organised by the Computer Science and Management Systems Departments Faculty of Computer Science and Management Wroclaw University of Technology and Sciences and University of Applied Sciences in Nysa Poland The papers have been grouped into three major parts Part I discusses topics including but not limited to Artificial Intelligence Methods Knowledge Discovery and Data Mining Big Data Knowledge Based Management Internet of Things Cloud Computing and High Performance Computing Distributed Computer Systems Content Delivery Networks and Service Oriented Computing Part II addresses topics including but not limited to System Modelling for Control Recognition and Decision Support Mathematical Modelling in Computer System Design Service Oriented Systems and Cloud Computing and Complex Process Modelling Part III focuses on topics including but not limited to Knowledge Based Management Modelling of Financial and Investment Decisions Modelling of Managerial Decisions Production Systems Management and Maintenance Risk Management Small Business Management and Theories and Models of Innovation

Loop Tiling for Parallelism Jingling Xue, 2012-12-06 Loop tiling as one of the most important compiler optimizations is beneficial for both parallel machines and uniprocessors with a memory hierarchy This book explores the use of loop tiling for reducing communication cost and improving parallelism for distributed memory machines The author provides mathematical foundations investigates loop permutability in the framework of nonsingular loop transformations discusses the necessary machineries required and presents state of the art results for finding communication and time minimal tiling choices Throughout the book theorems and algorithms are illustrated with numerous examples and diagrams The techniques presented in *Loop Tiling for Parallelism* can be adapted to work for a cluster of workstations and are also directly applicable to shared memory machines once the machines are modeled as BSP Bulk Synchronous Parallel machines Features and key topics Detailed review of the mathematical foundations including convex polyhedra and cones Self contained treatment of nonsingular loop transformations code generation and full loop permutability Tiling loop nests by rectangles and

parallelepipeds including their mathematical definition dependence analysis legality test and code generation A complete suite of techniques for generating SPMD code for a tiled loop nest Up to date results on tile size and shape selection for reducing communication and improving parallelism End of chapter references for further reading Researchers and practitioners involved in optimizing compilers and students in advanced computer architecture studies will find this a lucid and well presented reference work with numerous citations to original sources **Compiler Construction** David A. Watt, 2003-06-29 ETAPS2000 was the third instance of the European Joint Conferences on Theory and Practice of Software ETAPS is an annual federated conference that was established in 1998 by combining a number of existing and new conferences This year it comprised five conferences FOSSACS FASE ESOP CC TACAS and satellite workshops CBS CMCS CoFI GRATRA INT seven invited lectures a panel discussion and ten tutorials The events that comprise ETAPS address various aspects of the system development process including specification design implementation analysis and improvement The languages methodologies and tools which support these activities are all well within its scope Different blends of theory and practice are represented with an inclination towards theory with a practical motivation on one hand and soundly based practice on the other Many of the issues involved in software design apply to systems in general including hardware systems and the emphasis on software is not intended to be exclusive Perspectives of Systems Informatics Andrei Voronkov, Irina Virbitskaite, 2007-08-04 This book constitutes the thoroughly refereed post conference proceedings of the 6th International Andrei Ershov Memorial Conference PSI 2006 held in Akademgorodok Novosibirsk Russia in June 2006 The 30 revised full papers and 10 revised short papers presented together with 5 invited papers address all current aspects of theoretical computer science programming methodology and new information technologies

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