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HARDWARE/FIRMWARE INTERFACE DESIGN

BEST PRACTICES FOR IMPROVING EMBEDDED SYSTEMS DEVELOPMENT

- Reduce product development delays of ASICs, ASSPs, SoCs, and FPGAs
- Principles and best practices taught and illustrated
- Provides answers to common problems and pitfalls, saving time and effort

Gary Stringham

Yan Bai

Hardwarefirmware Interface Design Best Practices For Improving Embedded Systems Development:		

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