

HARDWARE SECURITY

Design, Threats,
and Safeguards

DebdEEP Mukhopadhyay
Rajat Subhra Chakraborty



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Hardware Security Design Threats And Safeguards

G Psacharopoulos



Hardware Security Design Threats And Safeguards:

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Split Manufacturing of Integrated Circuits for Hardware Security and Trust Ranga Vemuri, Suyuan Chen, 2021-05-25 Globalization of the integrated circuit IC supply chains led to many potential vulnerabilities Several attack scenarios can exploit these vulnerabilities to reverse engineer IC designs or to insert malicious trojan circuits Split manufacturing refers to the process of splitting an IC design into multiple parts and fabricating these parts at two or more foundries such that the design is secure even when some or all of those foundries are potentially untrusted Realizing its security benefits researchers have proposed split fabrication methods for 2D 2.5D and the emerging 3D ICs Both attack methods against split designs and defense techniques to thwart those attacks while minimizing overheads have steadily progressed over the past decade This book presents a comprehensive review of the state of the art and emerging directions in design splitting for secure split fabrication design recognition and recovery attacks against split designs and design techniques to defend against those attacks Readers will learn methodologies for secure and trusted IC design and fabrication using split design methods to protect against supply chain vulnerabilities

Fault Tolerant Architectures for Cryptography and Hardware Security SIKHAR PATRANABIS, Debdeep Mukhopadhyay, 2018-03-29 This book uses motivating examples and real life attack scenarios to introduce readers to the general concept of fault attacks in cryptography It offers insights into how the fault tolerance theories developed in the book can actually be implemented with a particular focus on a wide spectrum of fault models and practical fault injection techniques ranging from simple low cost techniques to high end equipment based methods It then individually examines fault attack vulnerabilities in symmetric asymmetric and authenticated encryption systems This is followed by extensive coverage

of countermeasure techniques and fault tolerant architectures that attempt to thwart such vulnerabilities Lastly it presents a case study of a comprehensive FPGA based fault tolerant architecture for AES 128 which brings together of a number of the fault tolerance techniques presented It concludes with a discussion on how fault tolerance can be combined with side channel security to achieve protection against implementation based attacks The text is supported by illustrative diagrams algorithms tables and diagrams presenting real world experimental results

Security in Computing and Communications Jemal H. Abawajy, Sougata Mukherjea, Sabu M. Thampi, Antonio Ruiz-Martínez, 2015-08-07 This book constitutes the refereed proceedings of the International Symposium on Security in Computing and Communications SSCC 2015 held in Kochi India in August 2015 The 36 revised full papers presented together with 13 short papers were carefully reviewed and selected from 157 submissions The papers are organized in topical sections on security in cloud computing authentication and access control systems cryptography and steganography system and network security application security

Fundamentals of IP and SoC Security Swarup Bhunia, Sandip Ray, Susmita Sur-Kolay, 2017-01-24 This book is about security in embedded systems and it provides an authoritative reference to all aspects of security in system on chip SoC designs The authors discuss issues ranging from security requirements in SoC designs definition of architectures and design choices to enforce and validate security policies and trade offs and conflicts involving security functionality and debug requirements Coverage also includes case studies from the trenches of current industrial practice in design implementation and validation of security critical embedded systems Provides an authoritative reference and summary of the current state of the art in security for embedded systems hardware IPs and SoC designs Takes a cross cutting view of security that interacts with different design and validation components such as architecture implementation verification and debug each enforcing unique trade offs Includes high level overview detailed analysis on implementation and relevant case studies on design verification debug issues related to IP SoC security

Formal Techniques for Safety-Critical Systems Cyrille Artho, Peter Csaba Ölveczky, 2017-02-15 This book constitutes the refereed proceedings of the 5th International Workshop on Formal Techniques for Safety Critical Systems FTSCS 2016 held in Tokyo Japan in November 2016 The 9 revised full papers presented together with an abstract of an invited talk were carefully reviewed and selected from 23 submissions The papers are organized in topical sections on specification and verification automotive and railway systems circuits and cyber physical systems parametrized verification

Encyclopedia of Cryptography, Security and Privacy Sushil Jajodia, Pierangela Samarati, Moti Yung, 2025-01-10 A rich stream of papers and many good books have been written on cryptography security and privacy but most of them assume a scholarly reader who has the time to start at the beginning and work his way through the entire text The goal of Encyclopedia of Cryptography Security and Privacy Third Edition is to make important notions of cryptography security and privacy accessible to readers who have an interest in a particular concept related to these areas but who lack the time to study one of the many books in these areas The third edition is intended as a replacement of Encyclopedia of Cryptography

and Security Second Edition that was edited by Henk van Tilborg and Sushil Jajodia and published by Springer in 2011 The goal of the third edition is to enhance on the earlier edition in several important and interesting ways First entries in the second edition have been updated when needed to keep pace with the advancement of state of the art Second as noticeable already from the title of the encyclopedia coverage has been expanded with special emphasis to the area of privacy Third considering the fast pace at which information and communication technology is evolving and has evolved drastically since the last edition entries have been expanded to provide comprehensive view and include coverage of several newer topics

Security, Privacy, and Applied Cryptography Engineering Sk Subidh Ali, Jean-Luc Danger, Thomas Eisenbarth, 2017-11-27 This book constitutes the refereed proceedings of the 7th International Conference on Security Privacy and Applied Cryptography Engineering SPACE 2017 held in Goa India in December 2017 The 13 revised full papers presented together with 1 short paper 7 invited talks and 4 tutorials were carefully reviewed and selected from 49 initial submissions This annual event is devoted to various aspects of security privacy applied cryptography and cryptographic engineering This is indeed a very challenging field requiring the expertise from diverse domains ranging from mathematics to solid state circuit design

Secure System Design and Trustable Computing Chip-Hong Chang, Miodrag Potkonjak, 2015-09-17 This book provides the foundations for understanding hardware security and trust which have become major concerns for national security over the past decade Coverage includes issues related to security and trust in a variety of electronic devices and systems related to the security of hardware firmware and software spanning system applications online transactions and networking services This serves as an invaluable reference to the state of the art research that is of critical significance to the security of and trust in modern society's microelectronic supported infrastructures

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