Ted Huffmire Cynthia Irvine Thuy D. Nguyen Timothy Levin Ryan Kastner Timothy Sherwood

Handbook of FPGA Design Security



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Handbook Of Fpga Design Security

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Handbook Of Fpga Design Security:

Handbook of FPGA Design Security Ted Huffmire, Cynthia Irvine, Thuy D. Nguyen, Timothy Levin, Ryan Kastner, Timothy Sherwood, 2010-06-18 The purpose of this book is to provide a practical approach to managing security in FPGA designs for researchers and practitioners in the electronic design automation EDA and FPGA communities including corporations industrial and government research labs and academics This book combines theoretical underpinnings with a practical design approach and worked examples for combating real world threats To address the spectrum of lifecycle and operational threats against FPGA systems a holistic view of FPGA security is presented from formal top level speci cation to low level policy enforcement mechanisms which integrates recent advances in the elds of computer security theory languages compilers and hardware The net effect is a diverse set of static and runtime techniques that working in coope tion facilitate the composition of robust dependable and trustworthy systems using commodity components We wish to acknowledge the many people who helped us ensure the success of ourworkonrecon gurablehardwaresecurity Inparticular wewishtothankAndrei Paun and Jason Smith of Louisiana Tech University for providing us with a Lin compatible version of Grail We also wish to thank those who gave us comments on drafts of this book including Marco Platzner of the University of Paderborn and Ali Irturk and Jason Oberg of the University of California San Diego This research was funded in part by Cyber Security and Safety of National Science Foundation Grant CNS 0524771 and NSF Career Grant CCF 0448654 Nuclear Power Plant Instrumentation and Control Systems Yastrebenetsky, Michael A., Kharchenko, Vyacheslav S., 2020-05-22 Safety and security are crucial to the operations of nuclear power plants but cyber threats to these facilities are increasing significantly Instrumentation and control systems which play a vital role in the prevention of these incidents have seen major design modifications with the implementation of digital technologies Advanced computing systems are assisting in the protection and safety of nuclear power plants however significant research on these computational methods is deficient Cyber Security and Safety of Nuclear Power Plant Instrumentation and Control Systems is a pivotal reference source that provides vital research on the digital developments of instrumentation and control systems for assuring the safety and security of nuclear power plants While highlighting topics such as accident monitoring systems classification measures and UAV fleets this publication explores individual cases of security breaches as well as future methods of practice This book is ideally designed for engineers industry specialists researchers policymakers scientists academicians practitioners and students involved in the development and operation of instrumentation and control systems for nuclear power plants chemical and petrochemical industries transport and medical equipment **Secure System Design and Trustable Computing** Chip-Hong Chang, Miodrag Potkonjak, 2015-09-17 This book provides the foundations for understanding hardware security and trust which have become major concerns for national security over the past decade Coverage includes issues related to security and trust in a variety of electronic devices and systems related to the security of hardware firmware

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invited talks by Claude and H l ne Kirchner and by Patrick Lincoln The foreword of this volume adds a brief overview of some of Jos s many scientific achievements followed by a bibliography of papers written by Jos Security Trends for FPGAS Benoit Badrignans, Jean Luc Danger, Viktor Fischer, Guy Gogniat, Lionel Torres, 2011-06-20 In Security Trends for FPGAs the authors present an analysis of current threats against embedded systems and especially FPGAs They discuss about requirements according to the FIPS standard in order to build a secure system This point is of paramount importance as it guarantees the level of security of a system Also highlighted are current vulnerabilities of FPGAs at all the levels of the security pyramid It is essential from a design point of view to be aware of all the levels in order to provide a comprehensive solution The strength of a system is defined by its weakest point there is no reason to enhance other protection means if the weakest point remains untreated Many severe attacks have considered this weakness in order not to face brute force attack complexity Several solutions are proposed in Security Trends for FPGA's especially at the logical architecture and system levels in order to provide a global solution Handbook of Networked and Embedded Control Systems Dimitrios Hristu-Varsakelis, William S. Levine, 2007-11-14 The vast majority of control systems built today are embedded that is they rely on built in special purpose digital computers to close their feedback loops Embedded systems are common in aircraft factories chemical processing plants and even in cars a single high end automobile may contain over eighty different computers The design of embedded controllers and of the intricate automated communication networks that support them raises many new questions practical as well as theoretical about network protocols compatibility of operating systems and ways to maximize the effectiveness of the embedded hardware This handbook the first of its kind provides engineers computer scientists mathematicians and students a broad comprehensive source of information and technology to address many questions and aspects of embedded and networked control Separated into six main sections Fundamentals Hardware Software Theory Networking and Applications this work unifies into a single reference many scattered articles websites and specification sheets Also included are case studies experiments and examples that give a multifaceted view of the subject encompassing computation and communication considerations **Nuclear Power Plant Instrumentation and Control** Systems for Safety and Security Yastrebenetsky, Michael A., Kharchenko, Vyacheslav, 2014-02-28 Accidents and natural disasters involving nuclear power plants such as Chernobyl Three Mile Island and the recent meltdown at Fukushima are rare but their effects are devastating enough to warrant increased vigilance in addressing safety concerns Nuclear Power Plant Instrumentation and Control Systems for Safety and Security evaluates the risks inherent to nuclear power and methods of preventing accidents through computer control systems and other such emerging technologies Students and scholars as well as operators and designers will find useful insight into the latest security technologies with the potential to make the future of nuclear energy clean safe and reliable The Industrial Electronics Handbook - Five Volume Set Bogdan M. Wilamowski, J. David Irwin, 2011-03-04 Industrial electronics systems govern so many different functions that vary

in complexity from the operation of relatively simple applications such as electric motors to that of more complicated machines and systems including robots and entire fabrication processes The Industrial Electronics Handbook Second Edition combines traditional and new Fundamentals of IP and SoC Security Swarup Bhunia, Sandip Ray, Susmita Sur-Kolay, 2017-01-24 This book is about security in embedded systems and it provides an authoritative reference to all aspects of security in system on chip SoC designs The authors discuss issues ranging from security requirements in SoC designs definition of architectures and design choices to enforce and validate security policies and trade offs and conflicts involving security functionality and debug requirements Coverage also includes case studies from the trenches of current industrial practice in design implementation and validation of security critical embedded systems Provides an authoritative reference and summary of the current state of the art in security for embedded systems hardware IPs and SoC designs Takes a cross cutting view of security that interacts with different design and validation components such as architecture implementation verification and debug each enforcing unique trade offs Includes high level overview detailed analysis on implementation and relevant case studies on design verification debug issues related to IP SoC security Computer Architecture Anupam Chattopadhyay, 2024-12-20 This handbook presents the key topics in the area of computer architecture covering from the basic to the most advanced topics including software and hardware design methodologies It will provide readers with the most comprehensive updated reference information covering applications in single core processors multicore processors application specific processors reconfigurable architectures emerging computing architectures processor design and programming flows test and verification This information benefits the readers as a full and guick technical reference with a high level review of computer architecture technology detailed technical descriptions and the latest practical applications Hardware Security Debdeep Mukhopadhyay, Rajat Subhra Chakraborty, 2014-10-29 Beginning with an introduction to cryptography Hardware Security Design Threats and Safeguards explains the underlying mathematical principles needed to design complex cryptographic algorithms It then presents efficient cryptographic algorithm implementation methods along with state of the art research and strategies for the design of very large scale integrated VLSI circuits and symmetric cryptosystems complete with examples of Advanced Encryption Standard AES ciphers asymmetric ciphers and elliptic curve cryptography ECC Gain a Comprehensive Understanding of Hardware Security from Fundamentals to Practical Applications Since most implementations of standard cryptographic algorithms leak information that can be exploited by adversaries to gather knowledge about secret encryption keys Hardware Security Design Threats and Safeguards Details algorithmic and circuit level countermeasures for attacks based on power timing fault cache and scan chain analysis Describes hardware intellectual property piracy and protection techniques at different levels of abstraction based on watermarking Discusses hardware obfuscation and physically unclonable functions PUFs as well as Trojan modeling taxonomy detection and prevention Design for Security and Meet Real Time Requirements If you consider

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Pedro C. Diniz,2007-03-19 This book constitutes the refereed proceedings of the Third International Workshop on Applied Reconfigurable Computing ARC 2007 held in Mangaratiba Brazil in March 2007 The 27 full papers and 10 short papers presented together with a late comer contribution from ARC 2006 are organized in topical sections on architectures mapping techniques and tools arithmetic and applications

Cryptographic Hardware and Embedded Systems - CHES 2006 Louis Goubin, Mitsuru Matsui, 2006-10-17 This book constitutes the refereed proceedings of the 8th International Workshop on Cryptographic Hardware and Embedded Systems CHES 2006 held in Yokohama Japan in October 2006 The 32 revised full papers presented together with three invited talks were carefully reviewed and selected from 112 submissions

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