

Hector Solar Ruiz · Roc Berenguer Pérez

Linear CMOS RF Power Amplifiers

A Complete Design Workflow

Linear Cmos Rf Power Amplifiers A Complete Design Workflow

Roman Wölfel



Linear Cmos Rf Power Amplifiers A Complete Design Workflow:

Linear CMOS RF Power Amplifiers Hector Solar Ruiz, Roc Berenguer Pérez, 2013-09-14 The work establishes the design flow for the optimization of linear CMOS power amplifiers from the first steps of the design to the final IC implementation and tests The authors also focus on design guidelines of the inductor's geometrical characteristics for power applications and covers their measurement and characterization Additionally a model is proposed which would facilitate designs in terms of transistor sizing required inductor quality factors or minimum supply voltage The model considers limitations that CMOS processes can impose on implementation The book also provides different techniques and architectures that allow for optimization Digitally Assisted, Fully Integrated, Wideband Transmitters for High-Speed Millimeter-Wave Wireless Communication Links

David del Rio, Ainhoa Rezola, Juan F. Sevillano, Igone Velez, Roc Berenguer, 2018-07-07 This book presents design methods and considerations for digitally assisted wideband millimeter wave transmitters It addresses comprehensively both RF design and digital implementation simultaneously in order to design energy and cost efficient high performance transmitters for mm wave high speed communications It covers the complete design flow from link budget assessment to the transistor level design of different RF front end blocks such as mixers and power amplifiers presenting different alternatives and discussing the existing trade offs The authors also analyze the effect of the imperfections of these blocks in the overall performance while describing techniques to correct and compensate for them digitally Well known techniques are revisited and some new ones are described giving examples of their applications and proving them in real integrated circuits **Linear CMOS RF Power Amplifiers for Wireless Applications**

Paulo Augusto Dal Fabbro, Maher Kayal, 2010-06-22 Advances in electronics have pushed mankind to create devices ranging from credible gadgets to medical equipment to spacecraft instruments More than that modern society is getting used to if not dependent on the comfort solutions and astonishing amount of information brought by these devices One field that has continuously benefited from those advances is the radio frequency integrated circuit RFIC design which in its turn has promoted countless benefits to the mankind as a payback Wireless communications is one prominent example of what the advances in electronics have enabled and their consequences to our daily life How could anyone back in the eighties think of the possibilities opened by the wireless local area networks WLANs that can be found today in a host of places such as public libraries coffee shops trains to name just a few How can a youngster who lives this true WLAN experience nowadays imagine a world without it This book deals with the design of linear CMOS RF Power Amplifiers PAs The RF PA is a very important part of the RF transceiver the device that enables wireless communications Two important aspects that are key to keep the advances in RF PA design at an accelerate pace are treated efficiency enhancement and frequency tunable capability For this purpose the design of two different integrated circuits realized in a 0.11 μm technology is presented each one addressing a different aspect With respect to efficiency enhancement the design of a dynamic supply RF power amplifier is treated making up the material of Chaps 2 to 4

RF CMOS Power Amplifiers: Theory, Design and Implementation Mona M. Hella, Mohammed Ismail, 2001-12-31 RF CMOS Power Amplifiers Theory Design and Implementation focuses on the design procedure and the testing issues of CMOS RF power amplifiers This is the first monograph addressing RF CMOS power amplifier design for emerging wireless standards The focus on power amplifiers for short is distance wireless personal and local area networks PAN and LAN however the design techniques are also applicable to emerging wide area networks WAN infrastructure using micro or pico cell networks The book discusses CMOS power amplifier design principles and theory and describes the architectures and tradeoffs in designing linear and nonlinear power amplifiers It then details design examples of RF CMOS power amplifiers for short distance wireless applications e g Bluetooth WLAN including designs for multi standard platforms Design aspects of RF circuits in deep submicron CMOS are also discussed RF CMOS Power Amplifiers Theory Design and Implementation serves as a reference for RF IC design engineers and RD and R D managers in industry and for graduate students conducting research in wireless semiconductor IC design in general and with CMOS technology in particular

High-Linearity CMOS RF Front-End Circuits Yongwang Ding, Ramesh Harjani, 2004-11-12 This book focuses on high performance radio frequency integrated circuits RF IC design in CMOS 1 Development of radio frequency ICs Wireless communications has been advancing rapidly in the past two decades Many high performance systems have been developed such as cellular systems AMPS GSM TDMA CDMA W CDMA etc GPS system global positioning system and WLAN wireless local area network systems The rapid growth of VLSI technology in both digital circuits and analog circuits provides benefits for wireless communication systems Twenty years ago not many people could imagine millions of transistors in a single chip or a complete radio for size of a penny Now not only complete radios have been put in a single chip but also more and more functions have been realized by a single chip and at a much lower price A radio transmits and receives electromagnetic signals through the air The signals are usually transmitted on high frequency carriers For example a typical voice signal requires only 30 Kilohertz bandwidth When it is transmitted by a FM radio station it is often carried by a frequency in the range of tens of megahertz to hundreds of megahertz Usually a radio is categorized by its carrier frequency such as 900 MHz radio or 5 GHz radio In general the higher the carrier frequency the better the directivity but the more difficult the radio design

Efficiency Enhancement and Frequency-tunable Capability in Linear CMOS RF Power Amplifiers for Wireless Applications Paulo Augusto Dal Fabbro, 2009

Linear and Switch-Mode RF Power Amplifiers Abdullah Eroglu, 2017-09-06 RF power amplifiers are implemented in communication semiconductor wafer processing magnetic resonance imaging MRI and radar systems to produce RF signal with the desired characteristics to perform several critical tasks in the entire system They can be designed to operate in linear or switch mode depending on the specific application This book explores the design and implementation methods for both linear and switch mode amplifiers with real world engineering problems The text discusses phased controlled switch mode amplifiers and distortion and modulation effects in RF amplifiers It illustrates the interface and

integration of components and sub systems for RF amplifiers The material is further reinforced with MATLAB design files

Modeling and Design Techniques for RF Power Amplifiers Arvind Raghavan, Nuttapong Srirattana, Joy Laskar, 2008-01-09
Achieve higher levels of performance integration compactness and cost effectiveness in the design and modeling of radio frequency RF power amplifiers RF power amplifiers are important components of any wireless transmitter but are often the limiting factors in achieving better performance and lower cost in a wireless communication system presenting the RF IC design community with many challenges The next generation technological advances presented in this book are the result of cutting edge research in the area of large signal device modeling and RF power amplifier design at the Georgia Institute of Technology and have the potential to significantly address issues of performance and cost effectiveness in this area Richly complemented with hundreds of figures and equations Modeling and Design Techniques for RF Power Amplifiers introduces and explores the most important topics related to RF power amplifier design under one concise cover With a focus on efficiency enhancement techniques and the latest advances in the field coverage includes Device modeling for CAD Empirical modeling of bipolar devices Scalable modeling of RF MOSFETs Power amplifier IC design Power amplifier design in silicon Efficiency enhancement of RF power amplifiers The description of state of the art techniques makes this book a valuable and handy reference for practicing engineers and researchers while the breadth of coverage makes it an ideal text for graduate and advanced undergraduate level courses in the area of RF power amplifier design and modeling **Scientific and Technical Aerospace Reports** ,1995 **Efficient Linear CMOS RF Power Amplification** Amirpouya Kavousian, 2007

The advent of high bandwidth highly spectral efficient communication protocols such as IEEE 802 11g has imposed tremendous challenges on RF power amplifier design Since the power amplifier is often the most power consuming block in a wireless system its efficiency can have a determining impact on the battery life of the system However the high linearity required in many current and emerging wireless systems has typically mandated the use of highly linear traditional class A designs that have a relatively low efficiency **CMOS RF Power Amplifier Design Approaches for Wireless Communications** Sataporn Pornpromlikit, 2010 This dissertation focuses on the design of CMOS power amplifiers for modern wireless handsets where stringent linearity requirements and high power efficiency are difficult to achieve simultaneously CMOS technology has been an attractive technology for research in fully integrated transceivers due to its low cost and high integration capability as well as its continuously improving high frequency performance Its advantages however come at the cost of continuously reduced breakdown voltages low isolation and high power loss in the substrate To address these limitations a stacked FET design technique is first developed to systematically divide the voltage stress among several transistors connected in series allowing the use of a larger supply voltage The voltage swing of each stacked device is added in phase to provide a larger output power to the load without the requirement of a large impedance transformation To investigate this technique a fully integrated 20 dBm RF power amplifier is first implemented using 0.25 μ m silicon on

sapphire MOSFETs By using triple stacked FETs the optimum load impedance for a 20 dBm power amplifier increases to 50 Ω so impedance transformation is not required at the output Measurement of a single stage linear power amplifier shows a small signal gain of 17.1 dB and a saturated output power of 21.0 dBm with a power added efficiency PAE of 44.0% at 1.88 GHz With an IS-95 code division multiple access CDMA modulated signal the power amplifier shows average output power of 16.3 dBm and PAE of 18.7% with ACPR below -42 dBc The concept is then further demonstrated at higher voltage and power level A single stage quadruple stacked FET linear power amplifier is presented using 0.28 μm 2.5 V standard I/O FETs in a 0.13 μm silicon on insulator SOI CMOS technology The PA is designed to withstand up to 9 V of supply voltage before reaching its breakdown limit The measured PA achieves a small signal gain of 14.6 dB a saturated output power of 32.4 dBm and a PAE of 47% at 1.9 GHz with a 6.5 V supply Using a reverse link IS-95 CDMA modulated signal the PA shows an average output power of up to 28.7 dBm with a PAE of 41.2% while meeting the adjacent channel power ratio requirement The PA also shows an average output power of up to 29.4 dBm with a PAE of 41.4% while meeting the adjacent channel leakage ratio requirement of an uplink wideband code division multiple access WCDMA modulated signal These performances are comparable to those of GaAs based power amplifiers To fully exploit the advantages of higher speed CMOS technology and the availability of co-integrated digital circuitry a digital intensive transceiver architecture is explored as an alternative in the second part of the dissertation A single ended digitally modulated power amplifier DPA is demonstrated in a 0.13 μm 1.2 V SOI CMOS technology to be used in a multi standard RF polar transmitter The amplitude modulation is done by digitally controlling the number of activated unit amplifiers whose currents are summed at the output The DPA is designed for multi mode multi band functionality by avoiding frequency selective components except for the final stage output matching network The measured DPA delivers a 24.9 dBm peak output power at 900 MHz with a maximum power efficiency of 62.7% Similar high efficiency performance is also exhibited at 1.92 GHz with a reconfigured matching network By employing a digital pre distortion technique the DPA could meet linearity requirements for both the enhanced data rate for GSM evolution EDGE and WCDMA standards

Introduction to RF Power Amplifier Design and Simulation Abdullah Eroglu, 2018-09-03 Introduction to RF Power Amplifier Design and Simulation fills a gap in the existing literature by providing step by step guidance for the design of radio frequency RF power amplifiers from analytical formulation to simulation implementation and measurement Featuring numerous illustrations and examples of real world engineering applications this book Gives an overview of intermodulation and elaborates on the difference between linear and nonlinear amplifiers Describes the high frequency model and transient characteristics of metal oxide semiconductor field effect transistors Details active device modeling techniques for transistors and parasitic extraction methods for active devices Explores network and scattering parameters resonators matching networks and tools such as the Smith chart Covers power sensing devices including four port directional couplers and new types of reflectometers Presents RF filter designs for power

amplifiers as well as application examples of special filter types Demonstrates the use of computer aided design CAD tools implementing systematic design techniques Blending theory with practice Introduction to RF Power Amplifier Design and Simulation supplies engineers researchers and RF microwave engineering students with a valuable resource for the creation of efficient better performing low profile high power RF amplifiers

Design of Linear RF Outphasing Power Amplifiers Xuejun Zhang, Lawrence E. Larson, Peter Asbeck, 2003 This is the first book devoted exclusively to the outphasing power amplifier covering the most recent research results on important aspects in practical design and applications A compilation of all the proposed outphasing approaches this is an important resource for engineers designing base station and mobile handset amplifiers engineering managers and program managers supervising power amplifier designs and R understand linearity and performance tradeoffs in microwave power amplifiers and understand the effect of new modulation techniques on microwave power amplifiers

Advanced Techniques in RF Power Amplifier Design Steve C. Cripps, 2002 This much anticipated volume builds on the author's popular work RF Power Amplifiers for Wireless Communications Artech House 1999 offering you a more in depth understanding of the theory and design of RF power amplifiers An invaluable reference tool for RF digital and system level designers the book enables you to efficiently design linear RF power amplifiers and includes detailed discussions on envelope power management schemes and linearization techniques

Highly Efficient Linear CMOS Power Amplifiers for Wireless Communications Ham Hee Jeon, 2012 The rapidly expanding wireless market requires low cost high integration and high performance of wireless communication systems CMOS technology provides benefits of cost effectiveness and higher levels of integration However the design of highly efficient linear CMOS power amplifier that meets the requirement of advanced communication standards is a challenging task because of the inherent difficulties in CMOS technology The objective of this research is to realize PAs for wireless communication systems that overcoming the drawbacks of CMOS process and to develop design approaches that satisfying the demands of the industry In this dissertation a cascode bias technique is proposed for improving linearity and reliability of the multi stage cascode CMOS PA In addition to achieve load variation immunity characteristic and to enhance matching and stability a fully integrated balanced PA is implemented in a 0.18 μ m CMOS process A triple mode balanced PA using switched quadrature coupler is also proposed and this work saved a large amount of quiescent current and further improved the efficiency in the back off power For the low losses and a high quality factor of passive output combining a transformer based quadrature coupler was implemented using integrated passive device IPD process Various practical approaches for linear CMOS PA are suggested with the verified results and they demonstrate the potential PA design approach for WCDMA applications using a standard CMOS technology

Design and Control of RF Power Amplifiers Alireza Shirvani, Bruce A. Wooley, 2013-04-18 Design and Control of RF Power Amplifiers investigates various architectures and concepts for the design and control of radio frequency RF power amplifiers This book covers merits and challenges of integrating RF power

amplifiers in various technologies and introduces a number of RF power amplifier performance metrics. It provides a thorough review of various power amplifier topologies followed by a description of approaches and architectures for the control and linearization of these amplifiers. A novel parallel amplifier architecture introduced in this book offers a breakthrough solution to enhancing efficiency in systems using power control. *Design and Control of RF Power Amplifiers* is a valuable resource for designers, researchers, and students in the field of RF integrated circuit design. Detailed and thorough coverage of various concepts in RF power amplifier design makes this book an invaluable guide for both beginners and professionals.

Highly Linear Integrated Wideband Amplifiers Henrik Sjolund, 1999-01-31. *Highly Linear Integrated Wideband Amplifiers: Design and Analysis Techniques for Frequencies from Audio to RF* deals with the complicated issues involved in the design of high linearity integrated wideband amplifiers for different operating frequencies. The book demonstrates these principles using a number of high performance designs. New topologies for high linearity are presented as well as a novel method for estimating the intermodulation distortion of a wideband signal. One of the most exciting results presented is an enhanced feedback configuration called feedback boosting that is capable of very low distortion. Also important is a statistical method for relating the intermodulation distortion of a wideband signal to the total harmonic distortion (THD) of a single tone. The THD as opposed to the intermodulation distortion of the wideband signal is easy to measure and use as a design parameter. Three different applications where high linearity is needed are identified, namely audio power amplifiers, wideband IF amplifiers, and RF power amplifiers. For these applications, high performance integrated amplifier designs using novel topologies are presented together with measurement results. The audio amplifiers are built in CMOS and are capable of driving 8Ω loudspeaker loads directly without using any external components. One of the designs can operate on a supply voltage down to 1.5V. Both bipolar and CMOS wideband IF amplifiers are built; they are fully differential and have linearity from DC to 20 MHz. Finally, an RF power amplifier is built in CMOS without using inductors in order to investigate what performance can be achieved without them. *Highly Linear Integrated Wideband Amplifiers: Design and Analysis Techniques for Frequencies from Audio to RF* is an excellent reference for researchers and designers of integrated amplifiers and may be used as a text for advanced courses on the topic.

RF Power Amplifiers for Mobile Communications Patrick Reynaert, Michiel Steyaert, 2006-11-18. This book tackles both high efficiency and high linearity power amplifier (PA) design in low voltage CMOS. With its emphasis on theory, design, and implementation, the book offers a guide for those actively involved in the design of fully integrated CMOS wireless transceivers. Offering mathematical background as well as intuitive insight, the book is essential reading for RF design engineers and researchers and is also suitable as a text book.

Index to IEEE Publications Institute of Electrical and Electronics Engineers, 1998. Issues for 1973 cover the entire IEEE technical literature.

CMOS RF Power Amplifier Design for Wireless Communications Qiang Fang, 2012.

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