

- Q1.** (a) (i) Obtain the partial differential equation governing the equations
 $f(u, v) = 0$, $u = x + yz$, $v = x + y + z$ 8
- (ii) Construct a partial differential equation of all surfaces of revolution having z-axis as the axis of rotation. 4

- (b) Discuss the phenomena of Hysteresis in ferromagnetic materials. On the B – H curve show the retentivity, coercivity and saturation points. What is coercive force and the energy dissipated per unit volume of the ferromagnetic substance during the hysteresis cycle ? 12
- (c) Explain and derive continuity of current equation using the principle of conservation of charge. 12
- (d) Using the principle of superposition, determine the current I in the 5 Ω resistor in the circuit shown in the figure. 12



- (e) State Ampere's circuital law. A hollow conducting cylinder has inner radius a and outer radius b and carries current I along the positive z -direction. Find \vec{H} everywhere. 12

- Q2.** (a) (i) If a square matrix A of order n with entries in field F has n distinct eigenvalues, then prove that matrix A is similar to a diagonal matrix. 10
- (ii) Find the matrix P which diagonalizes the matrix associated with the quadratic form

$$3x^2 + 5y^2 + 3z^2 - 2yz + 2zx - 2xy. \quad 10$$

- (b) (i) On the basis of specific resistance ρ , discuss the difference between conductors, semi-conductors and insulators. 8
- (ii) Prove that insulation resistance of a cable is inversely proportional to its length. Define insulation resistance. 12

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web jun 23 2022 the most multipurpose application of the phase locked loops pll is for clock generation and clock recovery in microprocessor networking communication systems and digital circuit and frequency

frequency synthesizer with accumulator based fractional n pll - Jul 15 2023

web description the fractional n pll with accumulator reference architecture uses a fractional clock divider with accumulator block as the frequency divider in a pll system the frequency divider divides the frequency of the vco output signal by a fractional value to make it comparable to a pfd reference signal frequency ports input expand all

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web a pll is a frequency synthesizer system that produces an output signal whose phase depends on the phase of its input signal in the simplest form a pll consists of a phase frequency detector pfd charge pump loop filter voltage controlled oscillator vco and a clock divider in a feedback loop

modeling of fractional n division frequency synthesizers with simulink - Nov 07 2022

web feb 1 2001 this paper describes a delta sigma delta sigma modulation and fractional n frequency division technique to perform indirect digital frequency synthesis based on the use of a phase locked loop pll