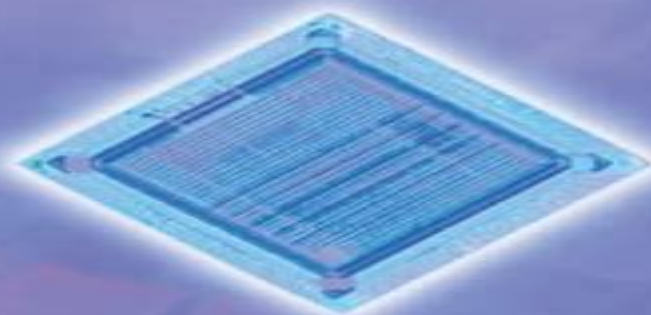


Low Power Methodology Manual

For System-on-Chip Design



Michael Keating David Flynn
Robert Aitken Alan Gibbons Kaijian Shi

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Low Power Methodology Manual For System On Chip Design Integrated Circuits And Systems

Ishfaq Ahmad, Sanjay Ranka



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