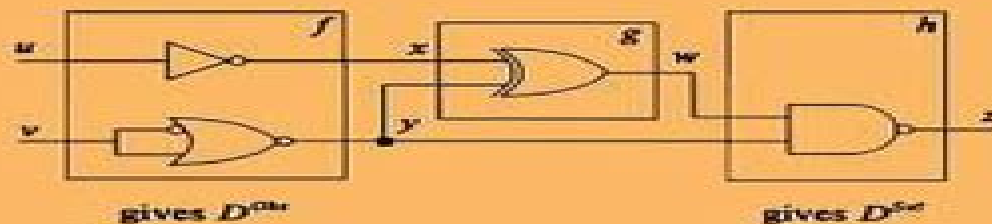
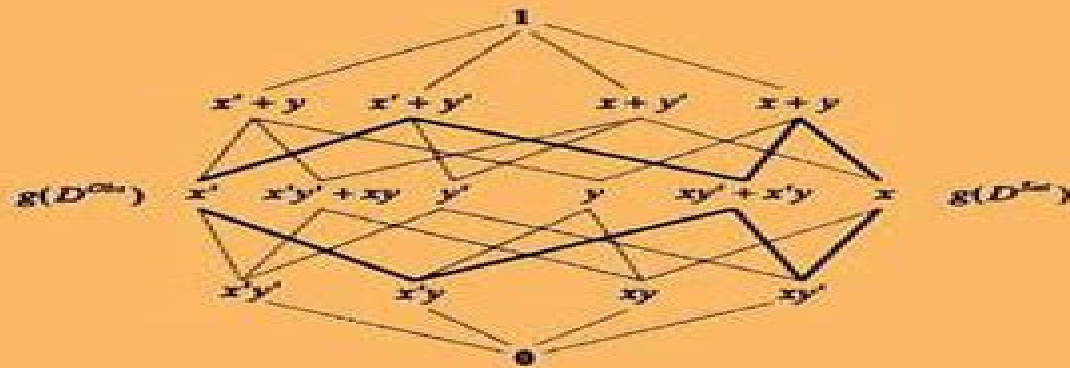


LOGIC SYNTHESIS AND VERIFICATION ALGORITHMS



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Fabio Somenzi

Logic Synthesis And Verification Algorithms

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Logic Synthesis And Verification Algorithms Gary, 2006-07-01 **Logic Synthesis and Verification Algorithms** Gary D. Hachtel, Fabio Somenzi, 2005-12-17 Logic Synthesis and Verification Algorithms is a textbook designed for courses on VLSI Logic Synthesis and Verification Design Automation CAD and advanced level discrete mathematics It also serves as a basic reference work in design automation for both professionals and students Logic Synthesis and Verification Algorithms is about the theoretical underpinnings of VLSI Very Large Scale Integrated Circuits It combines and integrates modern developments in logic synthesis and formal verification with the more traditional matter of Switching and Finite Automata Theory The book also provides background material on Boolean algebra and discrete mathematics A unique feature of this text is the large collection of solved problems Throughout the text the algorithms covered are the subject of one or more problems based on the use of available synthesis programs **Sequential Logic Synthesis** Pranav Ashar, S. Devadas, A. Richard Newton, 1992 Computer aided design CAD of very large scale integrated VLSI circuits is concerned with the development of computer programs for the automated design and manufacture of ICs Automated VLSI design is referred to as VLSI synthesis Synthesis of VLSI circuits involves transforming a specification of circuit behavior into a mask level layout which can be fabricated using VLSI manufacturing processes Optimization strategies are vital in VLSI synthesis in order to meet desired specifications However the optimization problems encountered in VLSI synthesis are typically nondeterministic polynomial time NP complete or NP hard Therefore solutions to the optimization problems incorporate heuristic strategies the development of which requires a thorough understanding of the problem at hand Thus optimization based VLSI synthesis has evolved into a rich and exciting area of research Automata theory forms a cornerstone of digital VLSI system design Sequential Logic Synthesis deals exclusively with finite automata theory and practice The extensive use of finite state automata finite state machines FSMs or simple sequential logic **Algorithms for VLSI Design Automation** Sabih H. Gerez, 1999-01-05 Modern microprocessors such as Intel's Pentium chip typically contain many millions of transistors They are known generically as Very Large Scale Integrated VLSI systems and their sheer scale and complexity has necessitated the development of CAD tools to automate their design This book focuses on the algorithms which are the building blocks of the design automation software which generates the layout of VLSI circuits Courses on this area are typically elective courses taken at senior undergrad or graduate level by students of Electrical and Electronic Engineering and sometimes in Computer Science or Computer Engineering Algorithms and Data Structures for Logic Synthesis and Verification Using Boolean Satisfiability Kevin W. DeRonne, 2013 **Logic Synthesis for Finite State Machines Based on Linear Chains of States** Alexander Barkalov, Larysa Titarenko, Jacek Bieganski, 2017-06-24 This book discusses Moore finite state machines FSMs implemented with field programmable gate arrays FPGAs including look up table LUT elements and embedded memory blocks EMBs To minimize the number of LUTs in FSM logic circuits the authors propose replacing a state

register with a state counter They also put forward an approach allowing linear chains of states to be created which simplifies the system of input memory functions and therefore decreases the number of LUTs in the resulting FSM circuit The authors combine this approach with using EMBs to implement the system of output functions microoperations This allows a significant decrease in the number of LUTs as well as eliminating a lot of interconnections in the FSM logic circuit As a rule it also reduces the area occupied by the circuit and diminishes the resulting power dissipation This book is an interesting and valuable resource for students and postgraduates in the area of computer science as well as for designers of digital systems that included complex control units

Algorithms and Data Structures for Logic Synthesis and Verification Using Boolean Satisfiability John D. Backes, 2013 *Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology* Luciano Lavagno, Igor L. Markov, Grant Martin, Louis K. Scheffer, 2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity Electronic Design Automation for IC Implementation Circuit Design and Process Technology provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

Boolean Models and Methods in Mathematics, Computer Science, and Engineering Yves Crama, Peter L. Hammer, 2010-06-28 A collection of papers written by prominent experts that examine a variety of advanced topics related to Boolean functions and expressions

EDA for IC Implementation, Circuit Design, and Process Technology Luciano Lavagno, Louis Scheffer, Grant Martin, 2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The second volume EDA for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic to GDSII a file format used to transfer data of semiconductor physical layout analog mixed signal design physical verification and technology CAD TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale power supply network design and analysis design modeling and much

more Save on the complete set **Rapid Prototyping of Digital Systems** James O. Hamblen, Michael D. Furman, 2007-05-08 Rapid Prototyping of Digital Systems provides an exciting and challenging laboratory component for undergraduate digital logic and computer design courses The more advanced topics and exercises also make this text useful for upper level courses in digital logic or programmable logic Design engineers working in industry will want to consider this text for a rapid introduction to PPLD technology and logic synthesis using commercial CAD tools Rapid Prototyping of Digital Systems includes two tutorials on the Altera CAD tool environment an overview of programmable logic and a design library with several easy to use input and output functions These features were developed to help students get started quickly Early design examples use schematic capture and library components VHDL is used for more complex designs after a short introduction to VHDL based synthesis The approach used in this text reflects contemporary practice in industry more accurately than the more traditional TTL protoboard based laboratory courses Designs containing up to twenty thousand gates are possible with the Altera Student Version CAD tools and the UP 1 board Rapid Prototyping of Digital Systems contains a number of interesting and challenging laboratory projects involving serial communications state machines with video output video games and graphics simple computers keyboard and mouse interfaces robotics and a RISC processor core These projects were all developed on the student version of the Altera CAD tools and can be implemented on the Altera UP 1 board **FPGA Design Automation** Deming Chen, Jason Cong, Peichen Pan, 2006 FPGA Design Automation A Survey is an up to date comprehensive survey tutorial of FPGA design automation with an emphasis on the recent developments within the past 5 to 10 years The focus is on the theory and techniques that have been or most likely will be reduced to practice It covers all major steps in FPGA design flow routing and placement circuit clustering technology mapping and architecture specific optimization physical synthesis RT level and behavior level synthesis and power optimization FPGA Design Automation A Survey can be used as both a guide for beginners who are embarking on research in this relatively young yet exciting area and a useful reference for established researchers in this field **Experimental and Efficient Algorithms** Sotiris E. Nikolettseas, 2005-05-03 This book constitutes the refereed proceedings of the 4th International Workshop on Experimental and Efficient Algorithms WEA 2005 held in Santorini Island Greece in May 2005 The 47 revised full papers and 7 revised short papers presented together with extended abstracts of 3 invited talks were carefully reviewed and selected from 176 submissions The book is devoted to the design analysis implementation experimental evaluation and engineering of efficient algorithms Among the application areas addressed are most fields applying advanced algorithmic techniques such as combinatorial optimization approximation graph theory discrete mathematics scheduling searching sorting string matching coding networking data mining data analysis etc **Handbook of Automated Reasoning** Alan J.A. Robinson, Andrei Voronkov, 2001-06-21 Handbook of Automated Reasoning **Binary Decision Diagrams** Rolf Drechsler, Bernd Becker, 2013-04-17 For someone with a hammer the whole world looks like a nail Within the last 10 13 years Binary Decision

Diagrams BDDs have become the state of the art data structure in VLSI CAD for representation and manipulation of Boolean functions Today BDDs are widely used and in the meantime have also been integrated in commercial tools especially in the area of verification and synthesis The interest in BDDs results from the fact that the data structure is generally accepted as providing a good compromise between conciseness of representation and efficiency of manipulation With increasing number of applications also in non CAD areas classical methods to handle BDDs are being improved and new questions and problems evolve and have to be solved The book should help the reader who is not familiar with BDDs or DDs in general to get a quick start On the other hand it will discuss several new aspects of BDDs e g with respect to minimization and implementation of a package This will help people working with BDDs in industry or academia to keep informed about recent developments in this area

Principles of Testing Electronic Systems Samiha Mourad, Yervant Zorian, 2000-07-25 A pragmatic approach to testing electronic systems As we move ahead in the electronic age rapid changes in technology pose an ever increasing number of challenges in testing electronic products Many practicing engineers are involved in this arena but few have a chance to study the field in a systematic way learning takes place on the job By covering the fundamental disciplines in detail Principles of Testing Electronic Systems provides design engineers with the much needed knowledge base Divided into five major parts this highly useful reference relates design and tests to the development of reliable electronic products shows the main vehicles for design verification examines designs that facilitate testing and investigates how testing is applied to random logic memories FPGAs and microprocessors Finally the last part offers coverage of advanced test solutions for today's very deep submicron designs The authors take a phenomenological approach to the subject matter while providing readers with plenty of opportunities to explore the foundation in detail Special features include An explanation of where a test belongs in the design flow Detailed discussion of scan path and ordering of scan chains BIST solutions for embedded logic and memory blocks Test methodologies for FPGAs A chapter on testing system on a chip Numerous references

Computational Intelligence in Digital and Network Designs and Applications Mourad Fakhfakh, Esteban Tlelo-Cuautle, Patrick Siarry, 2015-07-14 This book explains the application of recent advances in computational intelligence algorithms design methodologies and synthesis techniques to the design of integrated circuits and systems It highlights new biasing and sizing approaches and optimization techniques and their application to the design of high performance digital VLSI radio frequency and mixed signal circuits and systems This second of two related volumes addresses digital and network designs and applications with 12 chapters grouped into parts on digital circuit design network optimization and applications It will be of interest to practitioners and researchers in computer science and electronics engineering engaged with the design of electronic circuits

SOFSEM 2004: Theory and Practice of Computer Science P. van Emde Boas, 2004-01-14 This book constitutes the refereed proceedings of the 30th Conference on Current Trends in Theory and Practice of Computer Science SOFSEM 2004 held in Mer n Czech Republic in January 2004 The volume presents 10 invited

lectures and 22 full papers selected from 136 submissions Among the topics covered are computer science theory programming theory database systems information systems cognitive technologies and Web technologies SOFSEM 2004: Theory and Practice of Computer Science Peter Van Emde Boas, Jaroslav Pokorný, Mária Bielíková, Julius Stüller, 2003-12-22 This book constitutes the refereed proceedings of the 30th Conference on Current Trends in Theory and Practice of Computer Science SOFSEM 2004 held in Měrná Czech Republic in January 2004 The volume presents 10 invited lectures and 22 full papers selected from 136 submissions Among the topics covered are computer science theory programming theory database systems information systems cognitive technologies and Web technologies *New Data Structures and Algorithms for Logic Synthesis and Verification* Luca Gaetano Amaru, 2016-08-02 This book introduces new logic primitives for electronic design automation tools The author approaches fundamental EDA problems from a different unconventional perspective in order to demonstrate the key role of rethinking EDA solutions in overcoming technological limitations of present and future technologies The author discusses techniques that improve the efficiency of logic representation manipulation and optimization tasks by taking advantage of majority and biconditional logic primitives Readers will be enabled to accelerate formal methods by studying core properties of logic circuits and developing new frameworks for logic reasoning engines

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