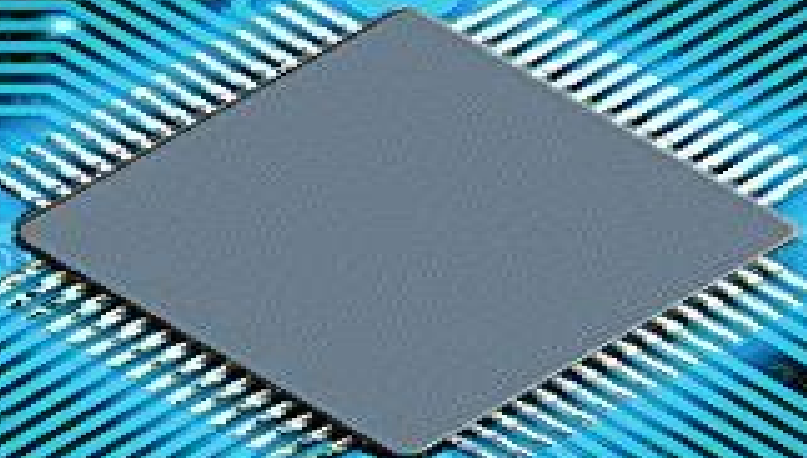


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Logic and Computer Design Fundamentals: Pearson New International Edition M. Morris Mano, Charles Kime, 2013-08-29 For one to two semester Computer Science and Engineering courses in logic and digital design Featuring a strong emphasis on the fundamentals underlying contemporary logic design using hardware description languages synthesis and verification this book focuses on the ever evolving applications of basic computer design concepts with strong connections to real world technology **Logic & Computer Design Fundamentals, 2/ed.** Mano, 2005 *Logic and Computer Design Fundamentals* M. Morris Mano, Charles R. Kime, 2004 *Digital Computer Design Fundamentals* Yaohan Chu, 1962 **Logic and Computer Design Fundamentals** M. Morris Mano, 2003 **AN INTRODUCTION TO DIGITAL COMPUTER DESIGN** V. RAJARAMAN, T. RADHAKRISHNAN, 2008-03-01 This highly acclaimed well established book now in its fifth edition is intended for an introductory course in digital computer design for B Sc students of computer science B Tech students of computer science and engineering and BCA MCA students of computer applications A knowledge of programming in C or Java would be useful to give the student a proper perspective to appreciate the development of the subject The first part of the book presents the basic tools and develops procedures suitable for the design of digital circuits and small digital systems It equips students with a firm understanding of logic principles before they study the intricacies of logic organization and architecture of computers in the second part Besides discussing data representation arithmetic operations Boolean algebra and its application in designing combinatorial and sequential switching circuits the book introduces the Algorithmic State Machines which are used to develop a hardware description language for the design of digital systems The organization of a small hypothetical computer is described to illustrate how instruction sets are evolved

Real computers namely Pentium and MIPS machines are described and compared with the hypothetical computer. After discussing the features of a CPU, I/O devices and I/O organization, cache and virtual memory, the book concludes with a new chapter on the use of parallelism to enhance the speed of computers. Besides the fifth edition has new material in CMOS gates, MSI ALU and Pentium5 architecture. The chapter on Cache and Virtual Memory has been rewritten.

Logic and Computer Design Fundamentals M. Morris Mano, 2013

Logic and Computer Design Fundamentals M. Morris Mano, Charles R. Kime, 2008

Featuring a strong emphasis on the fundamentals underlying contemporary logic design using hardware description languages, synthesis and verification, this book focuses on the ever-evolving applications of basic computer design concepts with strong connections to real-world technology. Treatment of logic design, digital system design and computer design. Ideal for self-study by engineers and computer scientists.

Logic and computer design fundamentals Moshe Morris Mano, Charles R. Kime, 1997

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M. Morris Mano, 1979 *Decision Diagram Techniques for Micro- and Nanoelectronic Design Handbook* Svetlana N. Yanushkevich, D. Michael Miller, Vlad P. Shmerko, Radomir S. Stankovic, 2018-10-03 Decision diagram DD techniques are very popular in the electronic design automation EDA of integrated circuits and for good reason They can accurately simulate logic design can show where to make reductions in complexity and can be easily modified to model different scenarios Presenting DD techniques from an applied perspective *Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook* provides a comprehensive up to date collection of DD techniques Experts with more than forty years of combined experience in both industrial and academic settings demonstrate how to apply the techniques to full advantage with more than 400 examples and illustrations Beginning with the fundamental theory data structures and logic underlying DD techniques they explore a breadth of topics from arithmetic and word level representations to spectral techniques and event driven analysis The book also includes abundant references to more detailed information and additional applications *Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook* collects the theory methods and practical knowledge necessary to design more advanced circuits and places it at your fingertips in a single concise reference **First**

International Conference on Sustainable Technologies for Computational Intelligence Ashish Kumar Luhach, Janos Arpad Kosa, Ramesh Chandra Poonia, Xiao-Zhi Gao, Dharm Singh, 2019-11-01 This book gathers high quality papers presented at the First International Conference on Sustainable Technologies for Computational Intelligence ICTSCI 2019 which was organized by Sri Balaji College of Engineering and Technology Jaipur Rajasthan India on March 29 30 2019 It covers emerging topics in computational intelligence and effective strategies for its implementation in engineering applications

Theory of Digital Automata Bohdan Borowik, Mykola Karpinsky, Valery Lahno, Oleksandr Petrov, 2012-11-08 This book serves a dual purpose firstly to combine the treatment of circuits and digital electronics and secondly to establish a strong connection with the contemporary world of digital systems The need for this approach arises from the observation that introducing digital electronics through a course in traditional circuit analysis is fast becoming obsolete Our world has gone

digital Automata theory helps with the design of digital circuits such as parts of computers telephone systems and control systems A complete perspective is emphasized because even the most elegant computer architecture will not function without adequate supporting circuits The focus is on explaining the real world implementation of complete digital systems In doing so the reader is prepared to immediately begin design and implementation work This work serves as a bridge to take readers from the theoretical world to the everyday design world where solutions must be complete to be successful

Progress in Pattern Recognition, Image Analysis, Computer Vision, and Applications Eduardo

Bayro-Corrochano, Edwin Hancock, 2014-10-23 This book constitutes the refereed proceedings of the 19th Iberoamerican Congress on Pattern Recognition CIARP 2014 held in Puerto Vallarta Jalisco Mexico in November 2014 The 115 papers presented were carefully reviewed and selected from 160 submissions The papers are organized in topical sections on image coding processing and analysis segmentation analysis of shape and texture analysis of signal speech and language document processing and recognition feature extraction clustering and classification pattern recognition and machine learning neural networks for pattern recognition computer vision and robot vision video segmentation and tracking

Design based Research Kirat Pal Singh, Author Impact Computers, Software Engineering, and Digital Devices Richard C.

Dorf, 2018-10-03 In two editions spanning more than a decade The Electrical Engineering Handbook stands as the definitive reference to the multidisciplinary field of electrical engineering Our knowledge continues to grow and so does the Handbook For the third edition it has expanded into a set of six books carefully focused on a specialized area or field of study Each book represents a concise yet definitive collection of key concepts models and equations in its respective domain thoughtfully gathered for convenient access Computers Software Engineering and Digital Devices examines digital and logical devices displays testing software and computers presenting the fundamental concepts needed to ensure a thorough understanding of each field It treats the emerging fields of programmable logic hardware description languages and parallel computing in detail Each article includes defining terms references and sources of further information Encompassing the work of the world's foremost experts in their respective specialties Computers Software Engineering and Digital Devices features the latest developments the broadest scope of coverage and new material on secure electronic commerce and parallel computing

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