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High Performance Computing

on Vector Systems 2007

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Galle, Wolfgang Bez, Hiroaki Kobayashi, 2009-10-07 This book covers the results of the Tera op Workbench other projects related to High Performance Computing and the usage of HPC installations at HLRS The Tera op Workbench project is a collaboration between the High Performance C puting Center Stuttgart HLRS and NEC Deutschland GmbH NEC HPCE to s port users in achieving their research goals using High Performance Computing The rst stage of the Tera op Workbench project 2004 2008 concentrated on user's applications and their optimization for the former ag ship of HLRS a node NEC SX 8 installation During this stage numerous individual codes dev oped and maintained by researchers or commercial organizations have been a lyzed and optimized Within the project several of the codes have shown the ability to outreach the TFlop's threshold of sustained performance This created the pos bility for new science and a deeper understanding of the underlying physics The second stage of the Tera op Workbench project 2008 2012 focuses on c rent and future trends of hardware and software developments We observe a strong tendency to heterogeneous environments on the hardware level while at the same time applications become increasingly heterogeneous by including multi physics or multi scale effects. The goal of the current studies of the Tera op Workbench is to gain insight in the developments of both components The overall target is to help scientists to run their application in the most of cient and most convenient way on the hardware best suited for their purposes High Performance Computing on Vector Systems 2010 Michael M. Resch, Katharina Benkert, Xin Wang, Martin Galle, Wolfgang Bez, Hiroaki Kobayashi, Sabine Roller, 2010-09-10 This book covers the results of the 11th and 12th Tera op Workshop and continued a series initiated by NEC and the HLRS in 2004 As part of the Tera op Workbench it has become a meeting platform for scientists application developers international experts and hardware designers to discuss the current state and future directions of supercomputing with the aim of achieving the highest sustained application perf mance The Tera op Workbench Project is a collaboration between the High Perf mance Computing Center Stuttgart HLRS and NEC Deutschland GmbH NEC HPCE to support users to achieve their research goals using High Performance Computing The rst stage of the Tera op Workbench project 2004 2008 c centrated on user s applications and their optimization for the 72 node NEC SX 8 installation at HLRS During this stage numerous individual codes developed and maintained by researchers or commercial organizations have been analyzed and timized Several of the codes have shown the ability to outreach the TFlop s thre old of sustained performance This created the possibility for new science and a deeper understanding of the underlying physics **High Performance Computing on Vector Systems 2005** Thomas Bönisch, Katharina Benkert, Toshiyuki Furui, Yoshiki Seo, Wolfgang Bez, 2007-05-31 The book presents the state of the art in high performance computing and simulation on modern supercomputer architectures It covers trends in hardware and software development in general and specifically the future of vector based systems and heterogeneous architectures The application contributions include computational fluid dynamics physics chemistry astrophysics and biology Innovative application fields like multiphysics simulations and material science are presented **High Performance Computing in**

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