

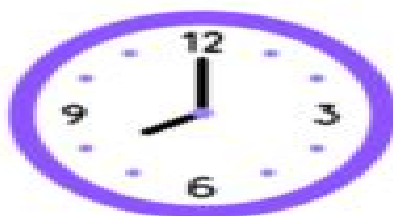
Name: _____

Date: _____

Matching Time to Clock Faces

Direction:

Draw a line to connect the clock with the matching time in the center.



4:00

10:00

8:00

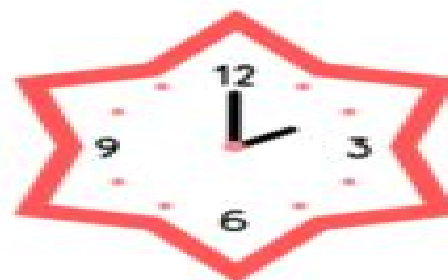
6:00

1:00

11:00

3:00

2:00



Matching Clock Faces To Time

Julia Schneider



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web the figure given below represents the functional block diagram of smps initially the unregulated ac input signal from the source is provided to the input rectifier and filter circuit here the ac input signal is rectified to generate a dc signal and further smoothened to remove high frequency noise component from it

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