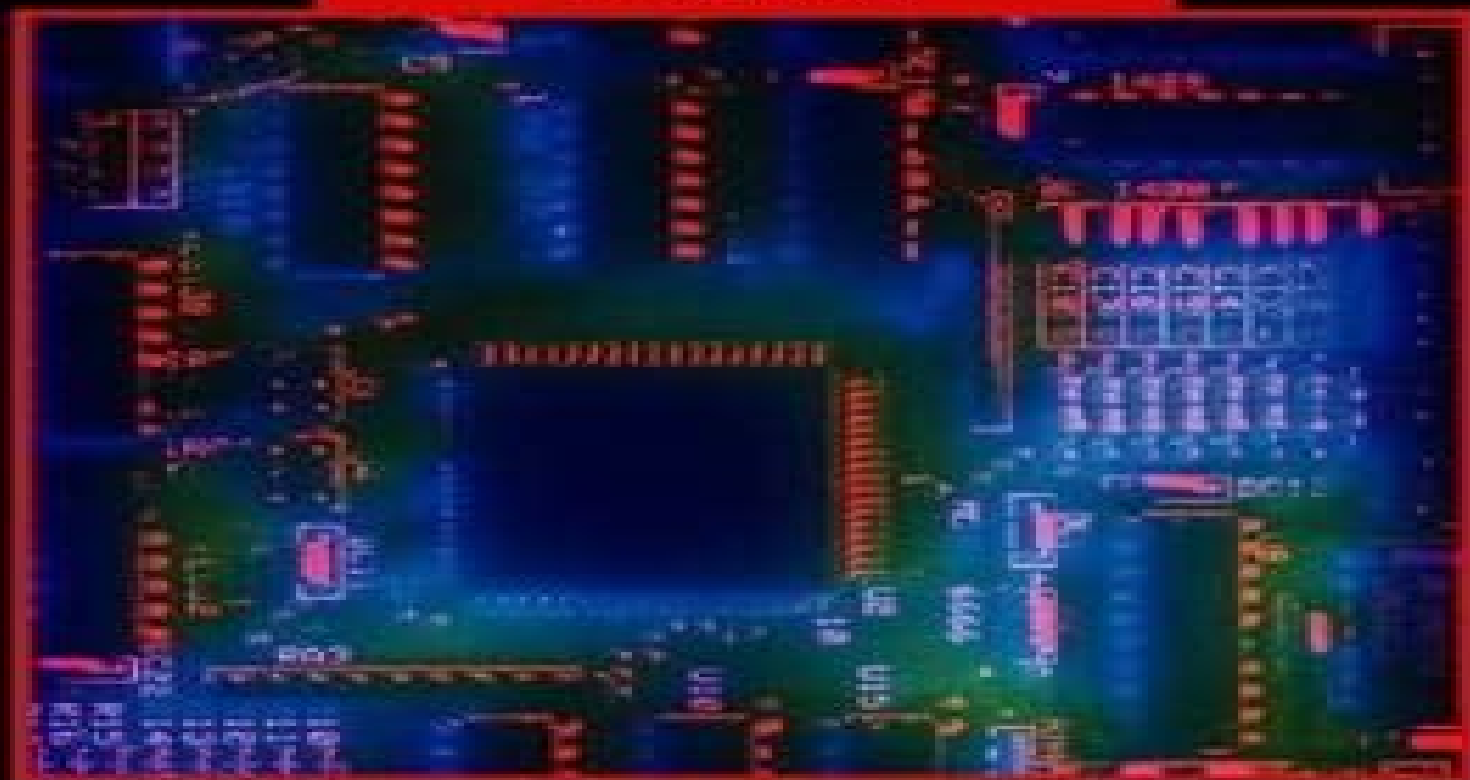


# LABORATORY MANUAL (PSpice® Emphasis) TO ACCOMPANY **ELECTRONIC DEVICES AND CIRCUIT THEORY**

Ninth Edition



Robert L. Boylestad | Louis Nashelsky | Franz J. Monssen

# Lab Manual Pspice Emphasis For Electronic Devices And Circuit Theory

**Christian Drosten**



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Analog Electronics T. E. Price, 1997. Analog Electronics: An integrated PSpice approach concentrates on the introductory material associated with analog electronics. The book starts with a description of the basic active devices: diodes, transistors (both bipolar and FET) and integrated circuits. There are chapters on frequency response, oscillators, filters, power supplies, and the interface between analog and digital circuitry. There are many worked examples and extensive use of PSpice provides realistic demonstrations of circuit principles.

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**American Journal of Physics**, 2002. **Scientific and Technical Aerospace Reports**, 1991. **Gallium Arsenide Digital Integrated Circuit Design** Stephen I. Long, 1990. *PSpice for Circuit Theory and Electronic Devices* Paul Tobin, 2022-05-31. PSpice for Circuit Theory and Electronic Devices is one of a series of five PSpice books and introduces the latest Cadence Orcad PSpice version 10.5 by simulating a range of DC and AC exercises. It is aimed primarily at those wishing to get up to speed with this version but will be of use to high school students, undergraduate students and of course lecturers. Circuit theorems are applied to a range of circuits and the calculations by hand after analysis are then compared to the simulated results. The Laplace transform and the s plane are used to analyze CR and LR circuits where transient signals are involved. Here the Probe output graphs demonstrate what a great learning tool PSpice is by providing the reader with a visual verification of any theoretical calculations. Series and parallel tuned resonant circuits are investigated where the difficult concepts of dynamic impedance and selectivity are best understood by sweeping different circuit parameters through a range of values. Obtaining semiconductor device characteristics as a laboratory exercise has fallen out of favour of late but nevertheless is still a useful exercise for understanding or modelling.

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