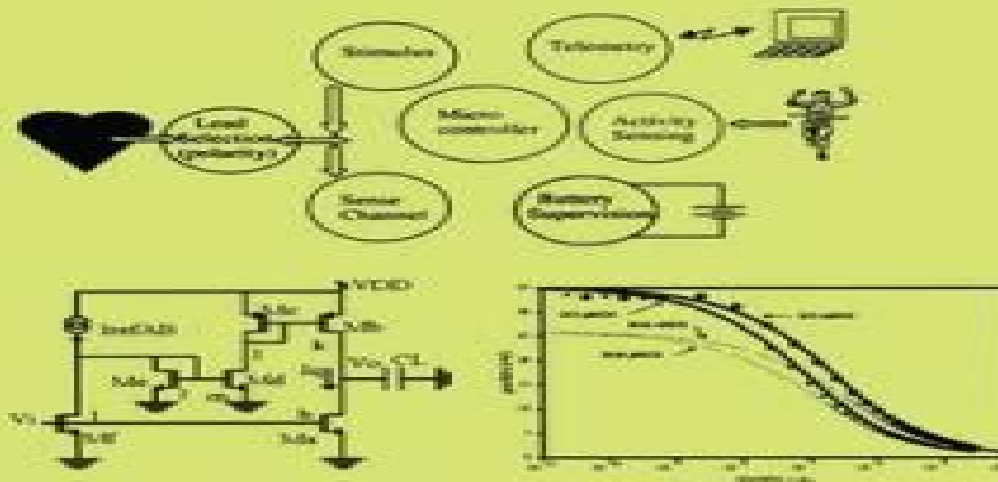


LOW POWER ANALOG CMOS FOR CARDIAC PACEMAKERS

Design and Optimization in Bulk and
SOI Technologies

Fernando Silveira and Denis Flandre



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