

Low-Voltage, Low-Power VLSI Subsystems

- ✓ New approaches to silicon-level design for low-power circuitry pacing electronics and ICs
- ✓ Low-power solutions for adders, multipliers, ROMs, SRAMs, DRAMs, and signal processors

Kiat-Seng Yeo
Kaushik Roy

Low Voltage Low Power Vlsi Subsystems

Katrin Zwirgmaier



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