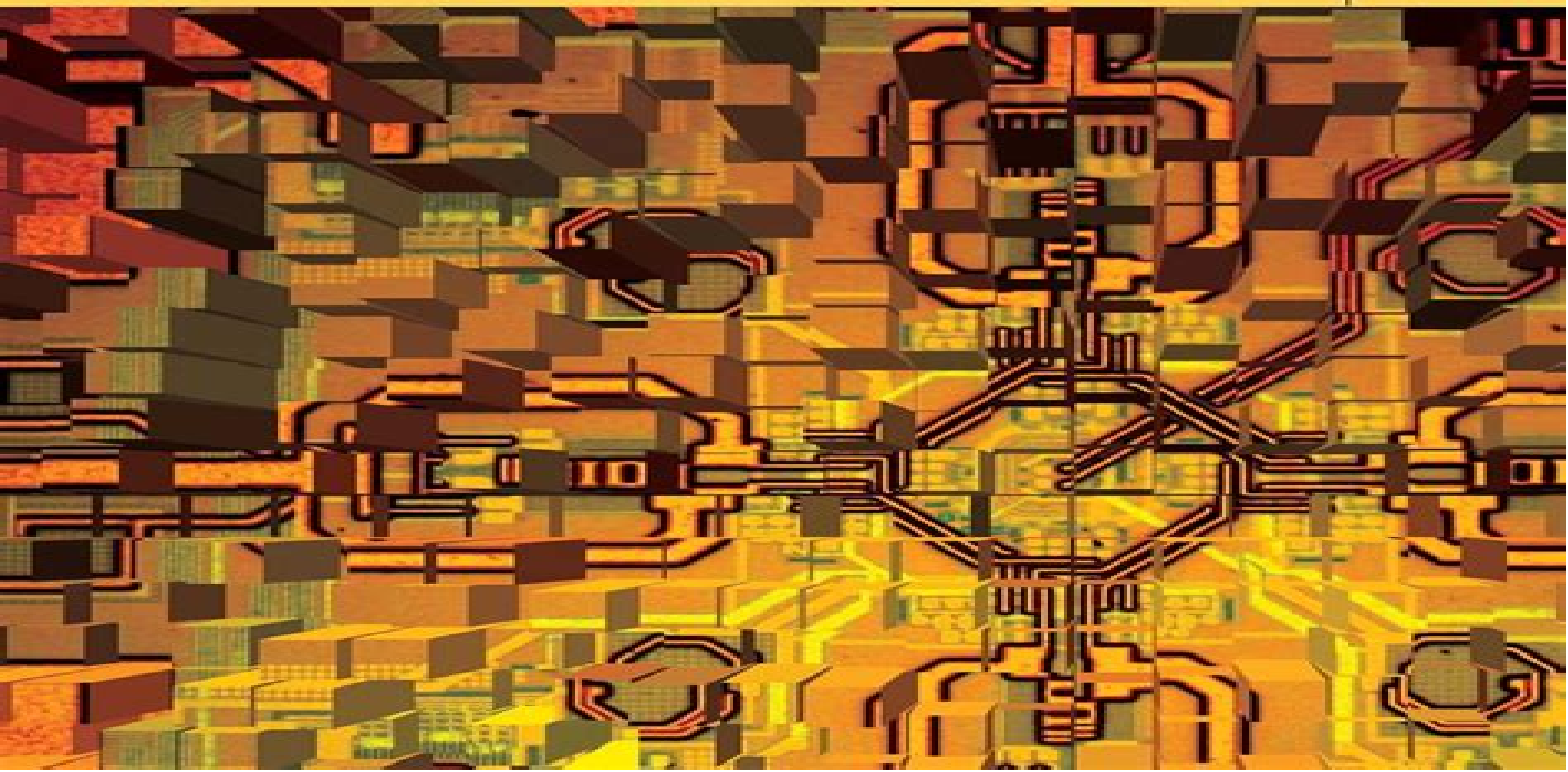


Integrated Frequency Synthesizers for Wireless Systems

Andrea Lacaita, Salvatore Levantino,
and Carlo Samori



Integrated Frequency Synthesizers For Wireless Systems

Integrated Frequency Synthesizers For Wireless Systems

Jad G. Atallah, Mohammed Ismail



Integrated Frequency Synthesizers For Wireless Systems Integrated Frequency Synthesizers For Wireless Systems:

Integrated Frequency Synthesizers for Wireless Systems Andrea Leonardo Lacaita, Salvatore Levantino, Carlo Samori, 2007-06-28 The increasingly demanding performance requirements of communications systems as well as problems posed by the continued scaling of silicon technology present numerous challenges for the design of frequency synthesizers in modern transceivers This book contains everything you need to know for the efficient design of frequency synthesizers for today s communications applications If you need to optimize performance and minimize design time you will find this book invaluable Using an intuitive yet rigorous approach the authors describe simple analytical methods for the design of phase locked loop PLL frequency synthesizers using scaled silicon CMOS and bipolar technologies The entire design process from system level specification to layout is covered comprehensively Practical design examples are included and implementation issues are addressed A key problem solving resource for practitioners in IC design the book will also be of interest to researchers and graduate students in electrical engineering

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Fully-integrated DLL/PLL-based CMOS Frequency Synthesizers for Wireless Systems Jaehyouk Choi, 2010 A frequency synthesizer plays a critical role in defining the performance of wireless systems in terms of measures such as operating frequency range settling time phase noise and spur performance and area power consumption As the trend in mobile system design has changed from single standard systems to multi standard multi application systems the role of frequency synthesizers has become even more important As the most popular architecture a phase locked loop PLL based frequency synthesizer has been researched over the last several decades however many unsolved problems related to the PLL based synthesizer are still waiting for answers This dissertation addresses key challenges related to fully integrated PLL based frequency synthesizers including the problem of large area consumption of passive components the inherent reference spur problem and the problem of trade offs between integer N PLLs and fractional N PLLs

Integrated Frequency Synthesis

for Convergent Wireless Solutions Jad G. Atallah, Mohammed Ismail, 2012-05-30 This book describes the design and implementation of an electronic subsystem called the frequency synthesizer which is a very important building block for any wireless transceiver. The discussion includes several new techniques for the design of such a subsystem which include the usage modes of the wireless device including its support for several leading edge wireless standards. This new perspective for designing such a demanding subsystem is based on the fact that optimizing the performance of a complete system is not always achieved by optimizing the performance of its building blocks separately. This book provides hands on examples of this sort of co design of optimized subsystems which can make the vision of an always best connected scenario a reality.

Design Of Cmos Rf Integrated Circuits And Systems Kiat Seng Yeo, Manh Anh Do, Chirn Chye Boon, 2010-03-24 This book provides the most comprehensive and in depth coverage of the latest circuit design developments in RF CMOS technology. It is a practical and cutting edge guide packed with proven circuit techniques and innovative design methodologies for solving challenging problems associated with RF integrated circuits and systems. This invaluable resource features a collection of the finest design practices that may soon drive the system on chip revolution. Using this book's state of the art design techniques one can apply existing technologies in novel ways and to create new circuit designs for the future.

Design of High-speed Communication Circuits Ramesh Harjani, 2006 MOS technology has rapidly become the de facto standard for mixed signal integrated circuit design due to the high levels of integration possible as device geometries shrink to nanometer scales. The reduction in feature size means that the number of transistor and clock speeds have increased significantly. In fact current day microprocessors contain hundreds of millions of transistors operating at multiple gigahertz. Furthermore this reduction in feature size also has a significant impact on mixed signal circuits. Due to the higher levels of integration the majority of ASICs possesses some analog components. It has now become nearly mandatory to integrate both analog and digital circuits on the same substrate due to cost and power constraints. This book presents some of the newer problems and opportunities offered by the small device geometries and the high levels of integration that is now possible. The aim of this book is to summarize some of the most critical aspects of high speed analog RF communications circuits. Attention is focused on the impact of scaling substrate noise data converters RF and wireless communication circuits and wireline communication circuits including high speed I/O. Contents: Achieving Analog Accuracy in Nanometer CMOS M. P. Flynn et al. Self Induced Noise in Integrated Circuits R. Gharpurey. High Speed Oversampling Analog to Digital Converters A. Gharbiya et al. Designing LC VCOs Using Capacitive Degeneration Techniques B. Jung. Fully Integrated Frequency Synthesizers A Tutorial S. T. Moon et al. Recent Advances and Design Trends in CMOS Radio Frequency Integrated Circuits D. J. Allstot et al. Equalizers for High Speed Serial Links P. K. Hanumolu et al. Low Power Parallel Interface with Continuous Time Adaptive Passive Equalizer and Crosstalk Cancellation C. P. Yue et al. Readership: Technologists, scientists and engineers in the field of high speed communication circuits. It can also be used as a textbook for graduate and advanced undergraduate.

courses *CMOS RF and mm-Wave Transceivers and Synthesizers* Bharatha Kumar Thangarasu, Nagarajan Mahalingam, Kaixue Ma, Kiat Seng Yeo, 2025-10-14 Power consumption has become a critical concern in RF mm wave integrated circuit IC design thanks to new applications from 5G mobile computing artificial intelligence and the Internet of Things However big challenges lie ahead for chip designers when they choose to develop ICs using silicon technology for low power and high data rate applications This is because silicon technology suffers from undesirable energy dissipation due to its lossy substrate and high resistive wiring loss at GHz frequencies Nonetheless silicon remains the most suitable material satisfying the demands of a rapidly growing semiconductor market through low fabrication cost and ease of achieving system on chip or system in package integration While long being neglected low power RF mm wave design has vaulted to the forefront of attention in recent years due to the demand for ultra low power transceivers to achieve sustainability Designing genuinely ubiquitous transceivers for these new applications requires innovations in both system architecture and circuit implementation This book closes the gap between a typical textbook with theories that are difficult to understand and a design oriented book that offers little insight into actual theories It evaluates and discusses different circuit topologies receiver and transmitter architectures phase locked loop performance metrics phase noise analysis and sub system level designs that have yet to be reported in other books **CMOS PLL Synthesizers: Analysis and Design** Keliu Shu, Edgar Sanchez-Sinencio, 2006-01-20 Thanks to the advance of semiconductor and communication technology the wireless communication market has been booming in the last two decades It evolved from simple pagers to emerging third generation 3G cellular phones In the meanwhile broadband communication market has also gained a rapid growth As the market always demands hi performance and low cost products circuit designers are seeking hi integration communication devices in cheap CMOS technology The phase locked loop frequency synthesizer is a critical component in communication devices It works as a local oscillator for frequency translation and channel selection in wireless transceivers and broadband cable tuners It also plays an important role as the clock synthesizer for data converters in the analog and digital signal interface This book covers the design and analysis of PLL synthesizers It includes both fundamentals and a review of the state of the art techniques The transient analysis of the third order charge pump PLL reveals its locking behavior accurately The behavioral level simulation of PLL further clarifies its stability limit Design examples are given to clearly illustrate the design procedure of PLL synthesizers A complete derivation of reference spurs in the charge pump PLL is also presented in this book The in depth investigation of the digital CA modulator for fractional N synthesizers provides insightful design guidelines for this important block Low-Voltage CMOS RF Frequency Synthesizers Howard Cam Luong, Gerry Chi Tak Leung, 2004-08-26 A frequency synthesizer is one of the most critical building blocks in any wireless transceiver system Its design is getting more and more challenging as the demand for low voltage low power high frequency wireless systems continuously grows As the supply voltage is decreased many existing design techniques are no longer applicable This book provides the reader with

architectures and design techniques to enable CMOS frequency synthesizers to operate at low supply voltage at high frequency with good phase noise and low power consumption In addition to updating the reader on many of these techniques in depth this book will also introduce useful guidelines and step by step procedure on behaviour simulations of frequency synthesizers Finally three successfully demonstrated CMOS synthesizer prototypes with detailed design consideration and description will be elaborated to illustrate potential applications of the architectures and design techniques described For engineers managers and researchers working in radio frequency integrated circuit design for wireless applications **RF / Microwave Circuit Design for Wireless Applications** Ulrich L. Rohde,Matthias Rudolph,2013-01-07 Provides researchers and engineers with a complete set of modeling design and implementation tools for tackling the newest IC technologies Revised and completely updated RF Microwave Circuit Design for Wireless Applications Second Edition is a unique state of the art guide to wireless integrated circuit design that provides researchers and engineers with a complete set of modeling design and implementation tools for tackling even the newest IC technologies It emphasizes practical design solutions for high performance devices and circuitry incorporating ample examples of novel and clever circuits from high profile companies Complete with excellent appendices containing working models and CAD based applications this powerful one stop resource Covers the entire area of circuit design for wireless applications Discusses the complete system for which circuits are designed as well as the device technologies on which the devices and circuits are based Presents theory as well as practical issues Introduces wireless systems and modulation types Takes a systematic approach that differentiates between designing for battery operated devices and base station design RF Microwave Circuit Design for Wireless Applications Second Edition is an indispensable tool for circuit designers engineers who design wireless communications systems and researchers in semiconductor technologies telecommunications and wireless transmission systems

Understanding Jitter and Phase Noise Nicola Da Dalt,Ali Sheikholeslami,2018-02-22 Gain an intuitive understanding of jitter and phase noise with this authoritative guide Leading researchers provide expert insights on a wide range of topics from general theory and the effects of jitter on circuits and systems to key statistical properties and numerical techniques Using the tools provided in this book you will learn how and when jitter and phase noise occur their relationship with one another how they can degrade circuit performance and how to mitigate their effects all in the context of the most recent research in the field Examine the impact of jitter in key application areas including digital circuits and systems data converters wirelines and wireless systems and learn how to simulate it using the accompanying Matlab code Supported by additional examples and exercises online this is a one stop guide for graduate students and practicing engineers interested in improving the performance of modern electronic circuits and systems *Advanced Frequency Synthesis by Phase Lock* William F. Egan,2011-08-09 The latest frequency synthesis techniques including sigma delta Diophantine and all digital Sigma delta is a frequency synthesis technique that has risen in popularity over the past decade due to its intensely digital

nature and its ability to promote miniaturization A continuation of the popular Frequency Synthesis by Phase Lock Second Edition this timely resource provides a broad introduction to sigma delta by pairing practical simulation results with cutting edge research Advanced Frequency Synthesis by Phase Lock discusses both sigma delta and fractional n the still in use forerunner to sigma delta employing Simulink models and detailed simulations of results to promote a deeper understanding After a brief introduction the book shows how spurs are produced at the synthesizer output by the basic process and different methods for overcoming them It investigates how various defects in sigma delta synthesis contribute to spurs or noise in the synthesized signal Synthesizer configurations are analyzed and it is revealed how to trade off the various noise sources by choosing loop parameters Other sigma delta synthesis architectures are then reviewed The Simulink simulation models that provided data for the preceding discussions are described providing guidance in making use of such models for further exploration Next another method for achieving wide loop bandwidth simultaneously with fine resolution the Diophantine Frequency Synthesizer is introduced Operation at extreme bandwidths is also covered further describing the analysis of synthesizers that push their bandwidths close to the sampling frequency limit Lastly the book reviews a newly important technology that is poised to become widely used in high production consumer electronics all digital frequency synthesis Detailed appendices provide in depth discussion on various stages of development and many related resources are available for download including Simulink models MATLAB scripts spreadsheets and executable programs All these features make this authoritative reference ideal for electrical engineers who want to achieve an understanding of sigma delta frequency synthesis and an awareness of the latest developments in the field

[Handbook of RF, Microwave, and Millimeter-wave Components](#) Leonid A. Belov, Sergey M. Smolskiy, Viktor Neofidovich Kochemasov, 2012 This unique and comprehensive resource offers you a detailed treatment of the operations principles key parameters and specific characteristics of active and passive RF microwave and millimeter wave components The book covers both linear and nonlinear components that are used in a wide range of application areas from communications and information sciences to avionics space and military engineering This practical book presents descriptions and clear examples and of the best materials and products used in the field including laminates prepregs substrates microstrip coaxial and waveguide transmission lines fixed and rotating connectors matching and adjusting elements frequency filters phase shifters and ferrite gates and circulators Moreover the book offers you in depth discussions on microwave switches and matrices including MEMS technology solid state and vacuum amplifiers mixers modulators and demodulators and oscillation sources You also find coverage of the stable frequency synthesizer structure and sources of modulated or noisy signals Greatly adding to the usefulness of this volume is the inclusion of more than 700 Internet addresses of manufacturers from across the globe

Wireless Technologies Krzysztof Iniewski, 2017-12-19 Advanced concepts for wireless technologies present a vision of technology that is embedded in our surroundings and practically invisible From established radio techniques like GSM 802 11 or Bluetooth to more

emerging technologies such as Ultra Wide Band and smart dust notes a common denominator for future progress is the underlying integrated circuit technology. Wireless Technologies responds to the explosive growth of standard cellular radios and radically different wireless applications by presenting new architectural and circuit solutions engineers can use to solve modern design problems. This reference addresses state of the art CMOS design in the context of emerging wireless applications including 3G 4G cellular telephony, wireless sensor networks and wireless medical application. Written by top international experts specializing in both the IC industry and academia, this carefully edited work uncovers new design opportunities in body area networks, medical implants, satellite communications, automobile radar detection and wearable electronics. The book is divided into three sections: wireless system perspectives, chip architecture and implementation issues and devices and technologies used to fabricate wireless integrated circuits. Contributors address key issues in the development of future silicon based systems such as scale of integration, ultra low power dissipation and the integration of heterogeneous circuit design style and processes onto one substrate. Wireless sensor network systems are now being applied in critical applications in commerce, healthcare and security. This reference which contains 25 practical and scientifically rigorous articles provides the knowledge communications engineers need to design innovative methodologies at the circuit and system level.

CMOS PLLs and VCOs for 4G Wireless Adem Aktas, Mohammed Ismail, 2007-05-08. CMOS PLLs and VCOs for 4G Wireless is the first book devoted to the subject of CMOS PLL and VCO design for future broadband 4th generation wireless devices. These devices will be handheld centric requiring very low power consumption and small footprint. They will be able to work across multiple bands and multiple standards covering WWAN, GSM, WCDMA, WLAN 802.11 a/b/g and WPAN, Bluetooth with different modulations, channel bandwidths, phase noise requirements etc. As such, this book discusses design modeling and optimization techniques for low power fully integrated broadband PLLs and VCOs in deep submicron CMOS. First, the PLL and VCO performances are studied in the context of the chosen multi band multi standard radio architecture and the adopted frequency plan. Next, a thorough study of the design requirements for broadband PLL/VCO design is conducted together with modeling techniques for noise sources in a PLL and VCO, focusing on optimization of integrated phase noise for multi carrier OFDM 64 QAM type applications. Design examples for multi standard 802.11a/b/g as well as for GSM, WCDMA are fully described and experimental results from 0.18 micron CMOS test chips have demonstrated the validity of the proposed design and optimization techniques. Equally important, the work describes techniques for robust high volume production of RF radios in general and for integrated PLL/VCO design in particular, including issues such as supply sensitivity, ground bounce and calibration mechanisms. CMOS PLLs and VCOs for 4G Wireless will be of interest to graduate students in electrical and computer engineering, design managers and RFIC designers in wireless semiconductor companies.

All-Digital Frequency Synthesizer in Deep-Submicron CMOS Robert Bogdan Staszewski, Poras T. Balsara, 2006-10-25. A new and innovative paradigm for RF frequency synthesis and wireless transmitter design. Learn the

techniques for designing and implementing an all digital RF frequency synthesizer In contrast to traditional RF techniques this innovative book sets forth digitally intensive design techniques that lead the way to the development of low cost low power and highly integrated circuits for RF functions in deep submicron CMOS processes Furthermore the authors demonstrate how the architecture enables readers to integrate an RF front end with the digital back end onto a single silicon die using standard ASIC design flow Taking a bottom up approach that progressively builds skills and knowledge the book begins with an introduction to basic concepts of frequency synthesis and then guides the reader through an all digital RF frequency synthesizer design Chapter 2 presents a digitally controlled oscillator DCO which is the foundation of a novel architecture and introduces a time domain model used for analysis and VHDL simulation Chapter 3 adds a hierarchical layer of arithmetic abstraction to the DCO that makes it easier to operate algorithmically Chapter 4 builds a phase correction mechanism around the DCO such that the system s frequency drift or wander performance matches that of the stable external frequency reference Chapter 5 presents an application of the all digital RF synthesizer Chapter 6 describes the behavioral modeling and simulation methodology used in design The final chapter presents the implementation of a full transmitter and experimental results The novel ideas presented here have been implemented and proven in two high volume commercial single chip radios developed at Texas Instruments Bluetooth and GSM While the focus of the book is on RF frequency synthesizer design the techniques can be applied to the design of other digitally assisted analog circuits as well This book is a must read for students and engineers who want to learn a new paradigm for RF frequency synthesis and wireless transmitter design using digitally intensive design techniques *Proceedings of MELECON ... ,2002* **Digital**

Subsampling Phase Lock Techniques for Frequency Synthesis and Polar Transmission Nereo Markulic,Kuba Raczkowski,Jan Craninckx,Piet Wambacq,2019-01-30 This book explains concepts behind fractional subsampling based frequency synthesis that is re shaping today s art in the field of low noise LO generation It covers advanced material giving clear guidance for development of background calibrated environments capable of spur free synthesis and wideband phase modulation It further expands the concepts into the field of subsampling polar transmission where the newly developed architecture enables unprecedented spectral efficiency levels unquestionably required by the upcoming generation of wireless standards *Radio-Frequency Integrated-Circuit Engineering* Cam Nguyen,2015-03-04 Radio Frequency Integrated Circuit Engineering addresses the theory analysis and design of passive and active RFIC s using Si based CMOS and Bi CMOS technologies and other non silicon based technologies The materials covered are self contained and presented in such detail that allows readers with only undergraduate electrical engineering knowledge in EM RF and circuits to understand and design RFICs Organized into sixteen chapters blending analog and microwave engineering Radio Frequency Integrated Circuit Engineering emphasizes the microwave engineering approach for RFICs Provides essential knowledge in EM and microwave engineering passive and active RFICs RFIC analysis and design techniques and RF systems vital for RFIC

students and engineers Blends analog and microwave engineering approaches for RFIC design at high frequencies Includes problems at the end of each chapter Digital Radio System Design Grigorios Kalivas, 2009-10-23 A systematic explanation of the principles of radio systems Digital Radio System Design offers a balanced treatment of both digital transceiver modems and RF front end subsystems and circuits It provides an in depth examination of the complete transceiver chain which helps to connect the two topics in a unified system concept Although the book tackles such diverse fields it treats them in sufficient depth to give the designer a solid foundation and an implementation perspective Covering the key concepts and factors that characterise and impact radio transmission and reception the book presents topics such as receiver design noise and distortion Information is provided about more advanced aspects of system design such as implementation losses due to non idealities Providing vivid examples illustrations and detailed case studies this book is an ideal introduction to digital radio systems design Offers a balanced treatment of digital modem and RF front end design concepts for complete transceivers Presents a diverse range of topics related to digital radio design including advanced transmission and synchronization techniques with emphasis on implementation Provides guidance on imperfections and non idealities in radio system design Includes detailed design case studies incorporating measurement and simulation results to illustrate the theory in practice

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