

```

const IppLibraryVersion *lib;
IppStatus status;
Ipp64u mask, emask;

/* Get Intel IPP Cryptography library version info */
lib = ippcpGetLibVersion();
printf("%s %s\n", lib->Name, lib->Version);

/* Get CPU features and features enabled with selected library level */
status = ippcpGetCpuFeatures( &mask );
if( ippStaNoErr == status ) {
    emask = ippcpGetEnabledCpuFeatures();
    printf("Features supported by CPU\tby Intel® Integrated Performance Primitives Cryptography\n");
    printf("-----\n");
    printf("  ippCPUID_MMX      = ");
    printf("%c\t%c\t", ( mask & ippCPUID_MMX ) ? 'Y':'N', ( emask & ippCPUID_MMX ) ? 'Y':'N');
    printf("Intel® Architecture MMX technology supported\n");
    printf("  ippCPUID_SSE      = ");
    printf("%c\t%c\t", ( mask & ippCPUID_SSE ) ? 'Y':'N', ( emask & ippCPUID_SSE ) ? 'Y':'N');
    printf("Intel® Streaming SIMD Extensions\n");
    printf("  ippCPUID_SSE2     = ");
    printf("%c\t%c\t", ( mask & ippCPUID_SSE2 ) ? 'Y':'N', ( emask & ippCPUID_SSE2 ) ? 'Y':'N');
    printf("Intel® Streaming SIMD Extensions 2\n");
    printf("  ippCPUID_SSE3     = ");
    printf("%c\t%c\t", ( mask & ippCPUID_SSE3 ) ? 'Y':'N', ( emask & ippCPUID_SSE3 ) ? 'Y':'N');
    printf("Intel® Streaming SIMD Extensions 3\n");
    printf("  ippCPUID_SSSE3    = ");
    printf("%c\t%c\t", ( mask & ippCPUID_SSSE3 ) ? 'Y':'N', ( emask & ippCPUID_SSSE3 ) ?
'Y':'N');
    printf("Supplemental Streaming SIMD Extensions 3\n");
    printf("  ippCPUID_MOVBE    = ");
    printf("%c\t%c\t", ( mask & ippCPUID_MOVBE ) ? 'Y':'N', ( emask & ippCPUID_MOVBE ) ?
'Y':'N');
    printf("The processor supports MOVBE instruction\n");
    printf("  ippCPUID_SSE41    = ");
    printf("%c\t%c\t", ( mask & ippCPUID_SSE41 ) ? 'Y':'N', ( emask & ippCPUID_SSE41 ) ?
'Y':'N');
    printf("Intel® Streaming SIMD Extensions 4.1\n");
    printf("  ippCPUID_SSE42    = ");
    printf("%c\t%c\t", ( mask & ippCPUID_SSE42 ) ? 'Y':'N', ( emask & ippCPUID_SSE42 ) ?
'Y':'N');

```

Intel Performance Primitives Manual

Nigel P. Smart



Intel Performance Primitives Manual:

Android Application Development for the Intel Platform Ryan Cohen, Tao Wang, 2014-09-17 The number of Android devices running on Intel processors has increased since Intel and Google announced in late 2011 that they would be working together to optimize future versions of Android for Intel Atom processors Today Intel processors can be found in Android smartphones and tablets made by some of the top manufacturers of Android devices such as Samsung Lenovo and Asus The increase in Android devices featuring Intel processors has created a demand for Android applications optimized for Intel Architecture Android Application Development for the Intel Platform is the perfect introduction for software engineers and mobile app developers Through well designed app samples code samples and case studies the book teaches Android application development based on the Intel platform including for smartphones tablets and embedded devices covering performance tuning debugging and optimization This book is jointly developed for individual learning by Intel Software College and China Shanghai JiaoTong University

Topics in Cryptology - CT-RSA 2018 Nigel P. Smart, 2018-04-07 This book constitutes the refereed proceedings of the Cryptographer s Track at the RSA Conference 2018 CT RSA 2018 held in San Francisco CA USA in March 2018 The 26 papers presented in this volume were carefully reviewed and selected from 79 submissions CT RSA is the track devoted to scientific papers on cryptography public key to symmetric key cryptography and from cryptographic protocols to primitives and their implementation security

Intel Integrated Performance Primitives Stewart Taylor, 2003 Annotation Introducing the basics of Intel Integrated Performance Primitives this book explores the range of possible applications from audio processing to graphics and video Included are extensive examples written in C on how to solve common imaging audio video and graphics problems using Intel IPP Demonstrated is how the IPP Programming interface can increase performance in Intel s latest microprocessors while providing time to market advantaged and reducing the cost of development Also covered is how Intel IPP provides access to advanced processor features without having to write processor specific code The accompanying CD ROM includes code samples and an evaluation version of the Intel Integrated Performance Primitives

Intelligent Computer Graphics 2010 Dimitri Plemenos, Georgios Miaoulis, 2010-09-30 Nowadays intelligent techniques are more and more used in Computer Graphics in order to optimise the processing time to find more accurate solutions for a lot of Computer Graphics problems than with traditional methods or simply to find solutions in problems where traditional methods fail The purpose of this volume is to present current work of the Intelligent Computer Graphics community a community growing up year after year This volume is a kind of continuation of the previously published Springer volumes Artificial Intelligence Techniques for Computer Graphics 2008 and Intelligent Computer Graphics 2009 2009 This volume contains selected extended papers from the last 3IA Conference 3IA 2010 which has been held in Athens Greece in May 2010 This year papers are particularly exciting and concern areas like rendering viewpoint quality data visualisation vision computational aesthetics scene understanding

intelligent lighting declarative modelling GIS scene reconstruction and other important themes

Partitioned convolution algorithms for real-time auralization Frank Wefers, 2015-05-11 This work discusses methods for efficient audio processing with finite impulse response FIR filters. Such filters are widely used for high quality acoustic signal processing e.g. for headphone or loudspeaker equalization in binaural synthesis in spatial sound reproduction techniques and for the auralization of reverberant environments. This work focuses on real time applications where the audio processing is subject to minimal delays/latencies. Different fast convolution concepts: transform based, interpolation based and number theoretic, which are used to implement FIR filters efficiently, are examined regarding their applicability in real time. These fast elementary techniques can be further improved by the concept of partitioned convolution. This work introduces a classification and a general framework for partitioned convolution algorithms and analyzes the algorithmic classes which are relevant for real time filtering. Elementary concepts which do not partition the filter impulse response e.g. regular Overlap Add and Overlap Save convolution and advanced techniques which partition filters uniformly and non uniformly. The algorithms are thereby regarded in their analytic complexity, their performance on target hardware, the optimal choice of parameters, assemblies of multiple filters, multi channel processing and the exchange of filter impulse responses without audible artifacts. Suitable convolution techniques are identified for different types of audio applications ranging from resource aware auralizations on mobile devices to extensive room acoustics audio rendering using dedicated multi processor systems.

Intelligent Systems Technologies and Applications Stefano Berretti, Sabu M. Thampi, Praveen Ranjan Srivastava, 2015-08-28 This book contains a selection of refereed and revised papers of Intelligent Techniques and Applications track and the Special Track on Intelligent Image Processing and Artificial Vision track originally presented at the International Symposium on Intelligent Systems Technologies and Applications ISTA August 10-13 2015 Kochi India.

High Performance Embedded Computing Handbook David R. Martinez, Robert A. Bond, M. Michael Vai, 2018-10-03 Over the past several decades applications permeated by advances in digital signal processing have undergone unprecedented growth in capabilities. The editors and authors of *High Performance Embedded Computing Handbook: A Systems Perspective* have been significant contributors to this field and the principles and techniques presented in the handbook are reinforced by examples drawn from their work. The chapters cover system components found in today's HPEC systems by addressing design trade-offs, implementation options and techniques of the trade, then solidifying the concepts with specific HPEC system examples. This approach provides a more valuable learning tool. Because readers learn about these subject areas through factual implementation cases drawn from the contributing authors' own experiences. Discussions include: Key subsystems and components; Computational characteristics of high performance embedded algorithms and applications; Front-end real-time processor technologies such as analog-to-digital conversion; application specific integrated circuits; field programmable gate arrays and intellectual property based design; Programmable HPEC systems technology including interconnection fabrics.

parallel and distributed processing performance metrics and software architecture and automatic code parallelization and optimization Examples of complex HPEC systems representative of actual prototype developments Application examples including radar communications electro optical and sonar applications The handbook is organized around a canonical framework that helps readers navigate through the chapters and it concludes with a discussion of future trends in HPEC systems The material is covered at a level suitable for practicing engineers and HPEC computational practitioners and is easily adaptable to their own implementation requirements

Computational Intelligence for Modelling and Prediction
Saman K. Halgamuge, Lipo Wang, 2005-09-02 The application of Computational Intelligence in emerging research areas such as Granular Computing Mechatronics and Bioinformatics shows its usefulness often emphasized by Prof Lotfi Zadeh the inventor of fuzzy logic and many others This book contains recent advances in Computational Intelligence methods for modeling optimization and prediction and covers a large number of applications The book presents new Computational Intelligence theory and methods for modeling and prediction The range of the various applications is captured with 5 chapters in image processing 2 chapters in audio processing 3 chapters in commerce and finance 2 chapters in communication networks and 6 chapters containing other applications

5G Wireless Systems
Yang Yang, Jing Xu, Guang Shi, Cheng-Xiang Wang, 2017-09-14 This book focuses on key simulation and evaluation technologies for 5G systems Based on the most recent research results from academia and industry it describes the evaluation methodologies in depth for network and physical layer technologies The evaluation methods are discussed in depth It also covers the analysis of the 5G candidate technologies and the testing challenges the evolution of the testing technologies fading channel measurement and modeling software simulations software hardware cosimulation field testing and other novel evaluation methods The fifth generation 5G mobile communications system targets highly improved network performances in terms of the network capacity and the number of connections Testing and evaluation technologies is widely recognized and plays important roles in the wireless technology developments along with the research on basic theory and key technologies The investigation and developments on the multi level and comprehensive evaluations for 5G new technologies provides important performance references for the 5G technology filtering and future standardizations Students focused on telecommunications electronic engineering computer science or other related disciplines will find this book useful as a secondary text Researchers and professionals working within these related fields will also find this book useful as a reference

Domain-Specific Program Generation
Christian Lengauer, 2004-05-24 Program generation holds the promise of helping to bridge the gap between application level problem solutions and efficient implementations at the level of today's source programs as written in C or Java Thus program generation can substantially contribute to reducing production cost and time to market in future software production while improving the quality and stability of the product This book is about domain specific program generation it is the outcome of a Dagstuhl seminar on the topic held in March 2003 After an introductory preface by the volume editors the 18 carefully

reviewed revised full papers presented are organized into topical sections on surveys of domain specific programming technologies domain specific programming languages tool support for program generation domain specific techniques for program optimization

Cosine-/Sine-Modulated Filter Banks Vladimir Britanak, K. R. Rao, 2017-08-02 This book covers various algorithmic developments in the perfect reconstruction cosine sine modulated filter banks TDAC MDCT MDST or MLT MCLT low delay MDCT complex exponential cosine sine modulated QMF filter banks and near perfect reconstruction QMF banks pseudo QMF banks in detail including their general mathematical properties matrix representations fast algorithms and various methods to integer approximations being recently a new transform technology for lossless audio coding Each chapter will contain a number of examples and will conclude with problems and exercises The book reflects the research efforts activities and achieved results of the authors in the time period over the last 20 years

Software Development for Embedded Multi-core Systems Max Domeika, 2011-04-08 The multicore revolution has reached the deployment stage in embedded systems ranging from small ultramobile devices to large telecommunication servers The transition from single to multicore processors motivated by the need to increase performance while conserving power has placed great responsibility on the shoulders of software engineers In this new embedded multicore era the toughest task is the development of code to support more sophisticated systems This book provides embedded engineers with solid grounding in the skills required to develop software targeting multicore processors Within the text the author undertakes an in depth exploration of performance analysis and a close up look at the tools of the trade Both general multicore design principles and processor specific optimization techniques are revealed Detailed coverage of critical issues for multicore employment within embedded systems is provided including the Threading Development Cycle with discussions of analysis design development debugging and performance tuning of threaded applications Software development techniques engendering optimal mobility and energy efficiency are highlighted through multiple case studies which provide practical how to advice on implementing the latest multicore processors Finally future trends are discussed including terascale speculative multithreading transactional memory interconnects and the software specific implications of these looming architectural developments This is the only book to explain software optimization for embedded multi core systems Helpful tips tricks and design secrets from an Intel programming expert with detailed examples using the popular X86 architecture Covers hot topics including ultramobile devices low power designs Pthreads vs OpenMP and heterogeneous cores

Intel Xeon Phi Processor High Performance Programming James Jeffers, James Reinders, Avinash Sodani, 2016-05-31 Intel Xeon Phi Processor High Performance Programming is an all in one source of information for programming the Second Generation Intel Xeon Phi product family also called Knights Landing The authors provide detailed and timely Knights Landingspecific details programming advice and real world examples The authors distill their years of Xeon Phi programming experience coupled with insights from many expert customers Intel Field Engineers Application Engineers and Technical Consulting Engineers to

create this authoritative book on the essentials of programming for Intel Xeon Phi products Intel Xeon Phi™ Processor High Performance Programming is useful even before you ever program a system with an Intel Xeon Phi processor To help ensure that your applications run at maximum efficiency the authors emphasize key techniques for programming any modern parallel computing system whether based on Intel Xeon processors Intel Xeon Phi processors or other high performance microprocessors Applying these techniques will generally increase your program performance on any system and prepare you better for Intel Xeon Phi processors A practical guide to the essentials for programming Intel Xeon Phi processors Definitive coverage of the Knights Landing architecture Presents best practices for portable high performance computing and a familiar and proven threads and vectors programming model Includes real world code examples that highlight usages of the unique aspects of this new highly parallel and high performance computational product Covers use of MCDRAM AVX 512 Intel Omni Path fabric many cores up to 72 and many threads 4 per core Covers software developer tools libraries and programming models Covers using Knights Landing as a processor and a coprocessor

Numerical Mathematics and Advanced Applications Alfredo Bermúdez de Castro,Dolores Gómez,Peregrina Quintela,Pilar Salgado,2007-10-08 These proceedings collect lectures given at ENUMATH 2005 the 6th European Conference on Numerical Mathematics and Advanced Applications held in Santiago de Compostela Spain in July 2005 Topics include applications such as fluid dynamics electromagnetism structural mechanics interface problems waves finance heat transfer unbounded domains numerical linear algebra convection diffusion as well as methodologies such as a posteriori error estimates discontinuous Galerkin methods multiscale methods optimization and more

Introduction to Autonomous Mobile Robots, second edition Roland Siegwart,Illah Reza Nourbakhsh,Davide Scaramuzza,2011-02-18 The second edition of a comprehensive introduction to all aspects of mobile robotics from algorithms to mechanisms Mobile robots range from the Mars Pathfinder mission s teleoperated Sojourner to the cleaning robots in the Paris Metro This text offers students and other interested readers an introduction to the fundamentals of mobile robotics spanning the mechanical motor sensory perceptual and cognitive layers the field comprises The text focuses on mobility itself offering an overview of the mechanisms that allow a mobile robot to move through a real world environment to perform its tasks including locomotion sensing localization and motion planning It synthesizes material from such fields as kinematics control theory signal analysis computer vision information theory artificial intelligence and probability theory The book presents the techniques and technology that enable mobility in a series of interacting modules Each chapter treats a different aspect of mobility as the book moves from low level to high level details It covers all aspects of mobile robotics including software and hardware design considerations related technologies and algorithmic techniques This second edition has been revised and updated throughout with 130 pages of new material on such topics as locomotion perception localization and planning and navigation Problem sets have been added at the end of each chapter Bringing together all aspects of mobile robotics into one volume Introduction to Autonomous Mobile Robots can

serve as a textbook or a working tool for beginning practitioners Curriculum developed by Dr Robert King Colorado School of Mines and Dr James Conrad University of North Carolina Charlotte to accompany the National Instruments LabVIEW Robotics Starter Kit are available Included are 13 6 by Dr King and 7 by Dr Conrad laboratory exercises for using the LabVIEW Robotics Starter Kit to teach mobile robotics concepts **The Computer Engineering Handbook** Vojin G. Oklobdzija,2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering The unparalleled rate of technological advancement the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own **Computational Science - ICCS 2008** ,2008 **Programming with Intel Wireless MMX Technology** Nigel C. Paver,Bradley C. Aldrich,Moinul H. Khan,2004 **Handbook of Parallel Computing and Statistics** Erricos John Kontoghiorghes,2005-12-21 Technological improvements continue to push back the frontier of processor speed in modern computers Unfortunately the computational intensity demanded by modern research problems grows even faster Parallel computing has emerged as the most successful bridge to this computational gap and many popular solutions have emerged based on its concepts ECOOP 2010 -- Object-Oriented Programming Theo D'Hondt,2010-06-29 Annotation This book constitutes the refereed proceedings of the 24th European Conference on Object Oriented Programming ECOOP 2010 held in Maribor Slovenia in June 2010 The 24 revised full papers presented together with one extended abstract were carefully reviewed and selected from a total of 108 submissions The papers cover topics such as programming environments and tools theoretical foundations of programming languages formal methods concurrency models in Java empirical methods type systems language design and implementation concurrency abstractions and experiences

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