

EXPERIMENT NO. 11

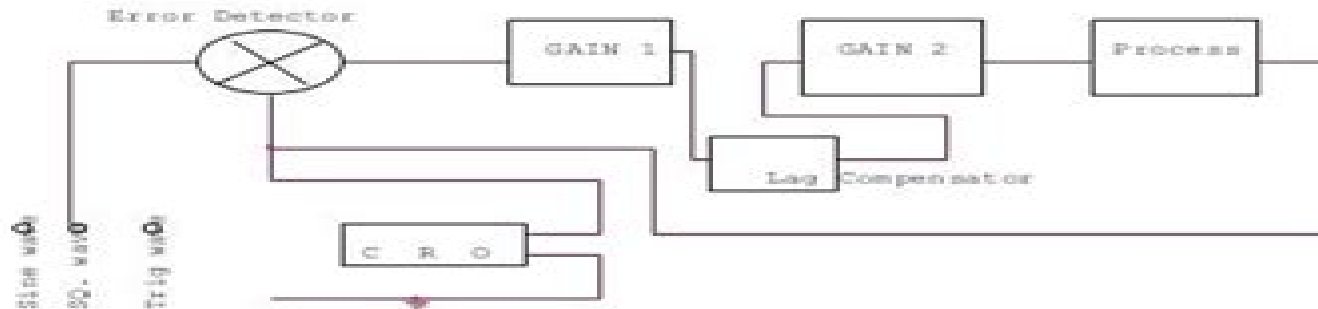
AIM: To study LEAD LAG compensator and draw magnitude and phase plot.

APPARATUS REQUIRED: lead lag compensator kit, CRO, and connecting leads.

THEORY:

Compensation network are often used to make improvement in transient response and small change in steady state accuracy. The set up is divided in to three parts. Signal source: It has sine wave of 10-1200 Hz. Of 0-8 Vpp, Square wave of 20, 40 and 80 Hz of 0-2 Vpp. Trigger is available for trigger of CRO in external trigger mode. The amplitude is 1.2 Vpp. There are three compensation circuits as lag, Lead and Lag-Lead with transfer function. The set up has two DC regulated power supply for signal source and systems.

CIRCUIT DIAGRAM



PROCEDURE: The experiment is divided in two parts.

1. Open Loop Response: Connect square wave to gain and CRO across input and output. Select frequency to 80 Hz at 0.2 Vpp. Measure input amplitude Vpp as A and output as amplitude as B. Calculate gain factor= B/A . Connect sine wave with process input, CRO across input and output. Set input voltage =8Vpp. From low frequency end 10Hz note output voltage Vpp as B. Note the phase difference for each test frequency. Connect the sine wave with lag input ,connect CRO across input and output. Note the output voltage, phase difference for each test frequency. Prepare a table between input/output voltage, gain magnitude in db and phase angle in degrees. Plot a graph accordingly.
2. Close loop response: connect the square wave signal of 20Hz, 1Vpp at input of error detector. Adjust gain to the value found from plot for required shape of response and sketch it 0 the paper. From the transient response measure maximum over shoot Mp, steady state error Ess and peak time Tp. Select wave frequency to

Instrumentation And Control Systems Lab Manual

F Rizvi



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