

# **Low-Power CMOS Circuits**

Technology, Logic Design  
and CAD Tools

**Christian Piguet**



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# Low Power Cmos Circuits Technology Logic Design And Cad Tools

**Weixia Xu,Liquan Xiao,Jinwen  
Li,Chengyi Zhang,Zhenzhen Zhu**



## **Low Power Cmos Circuits Technology Logic Design And Cad Tools:**

Low-Power CMOS Circuits Christian Piguet, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published *Low Power Electronics Design* *Low Power CMOS Circuits Technology Logic Design and CAD Tools* addresses the design of low power circuitry in deep submicron technologies It provides a focused reference for specialists involved in designing low power circuitry from transistors to logic gates The book is organized into three broad sections for convenient access The first examines the history of low power electronics along with a look at emerging and possible future technologies It also considers other technologies such as nanotechnologies and optical chips that may be useful in designing integrated circuits The second part explains the techniques used to reduce power consumption at low levels These include clock gating leakage reduction interconnecting and communication on chips and adiabatic circuits The final section discusses various CAD tools for designing low power circuits This section includes three chapters that demonstrate the tools and low power design issues at three major companies that produce logic synthesizers Providing detailed examinations contributed by leading experts *Low Power CMOS Circuits Technology Logic Design and CAD Tools* supplies authoritative information on how to design and model for high performance with low power consumption in modern integrated circuits It is a must read for anyone designing modern computers or embedded systems

**Low-Power CMOS Circuits** Christian Piguet, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published *Low Power Electronics Design* *Low Power CMOS Circuits Technology Logic Design and CAD Tools* addresses the design of low power circuitry in deep submicron technologies It provides a focused reference for specialists involved in designing low power circuitry from transistors to logic gates The book is organized into three broad sections for convenient access The first examines the history of low power electronics along with a look at emerging and possible future technologies It also considers other technologies such as nanotechnologies and optical chips that may be useful in designing integrated circuits The second part explains the techniques used to reduce power consumption at low levels These include clock gating leakage reduction interconnecting and communication on chips and adiabatic circuits The final section discusses various CAD tools for designing low power circuits This section includes three chapters that demonstrate the tools and low power design issues at three major companies that produce logic synthesizers Providing detailed examinations contributed by leading experts *Low Power CMOS Circuits Technology Logic Design and CAD Tools* supplies authoritative information on how to design and model for high performance with low power consumption in modern integrated circuits It is a must read for anyone designing modern computers or embedded systems

*Low-Power CMOS Design* Anantha Chandrakasan, Robert W. Brodersen, 1998-02-11 This collection of important papers provides a comprehensive overview of low power system design from component technologies and circuits to architecture system design

and CAD techniques LOW POWER CMOS DESIGN summarizes the key low power contributions through papers written by experts in this evolving field Low-Power Processors and Systems on Chips Christian Pigué, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Pigué's recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts Low Power Processors and Systems on Chips supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems *Green Mobile Devices and Networks* Hrishikesh Venkataraman, Gabriel-Miro Muntean, 2016-04-19 While battery capacity is often insufficient to keep up with the power demanding features of the latest mobile devices powering the functional advancement of wireless devices requires a revolution in the concept of battery life and recharge capability Future handheld devices and wireless networks should be able to recharge themselves automaticall

**Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology** Luciano Lavagno, Igor L. Markov, Grant Martin, Louis K. Scheffer, 2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity

Electronic Design Automation for IC Implementation Circuit Design and Process Technology provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals     **Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation** José L. Ayala, Delong Shang, Alex Yakovlev, 2013-01-03 This book constitutes the refereed proceedings of the 22nd International Conference on Integrated Circuit and System Design PATMOS 2012 held in Newcastle UK Spain in September 2012 The 25 revised full papers presented were carefully reviewed and selected from numerous submissions The paper feature emerging challenges in methodologies and tools for the design of upcoming generations of integrated circuits and systems including reconfigurable hardware such as FPGAs The technical program focus on timing performance and power consumption as well as architectural aspects with particular emphasis on modeling design characterization analysis and optimization     MOS Devices for Low-Voltage and Low-Energy Applications Yasuhisa Omura, Abhijit Mallik, Naoto Matsuo, 2017-02-28 Helps readers understand the physics behind MOS devices for low voltage and low energy applications Based on timely published and unpublished work written by expert authors Discusses various promising MOS devices applicable to low energy environmental and biomedical uses Describes the physical effects quantum tunneling of MOS devices Demonstrates the performance of devices helping readers to choose right devices applicable to an industrial or consumer environment Addresses some Ge based devices and other compound material based devices for high frequency applications and future development of high performance devices Seemingly innocuous everyday devices such as smartphones tablets and services such as on line gaming or internet keyword searches consume vast amounts of energy Even when in standby mode all these devices consume energy The upcoming Internet of Things IoT is expected to deploy 60 billion electronic devices spread out in our homes cars and cities Britain is already consuming up to 16 per cent of all its power through internet use and this rate is doubling every four years According to The UK s Daily Mail May 2015 if usage rates continue all of Britain s power supply could be consumed by internet use in just 20 years In 2013 U S data centers consumed an estimated 91 billion kilowatt hours of electricity corresponding to the power generated by seventeen 1000 megawatt nuclear power plants Data center electricity consumption is projected to increase to roughly 140 billion kilowatt hours annually by 2020 the equivalent annual output of 50 nuclear power plants Natural Resources Defense Council USA Feb 2015 All these examples stress the urgent need for developing electronic devices that consume as little energy as possible The book MOS Devices for Low Voltage and Low Energy Applications explores the different transistor options that can be utilized to achieve that goal It describes in detail the physics and performance of transistors that can be operated at low voltage and consume little power such as subthreshold operation in bulk transistors fully depleted SOI devices tunnel FETs multigate and gate all around MOSFETs Examples of low energy circuits making use of these devices are given as well The book MOS Devices for Low Voltage and Low Energy Applications is a good reference for graduate students researchers semiconductor and electrical engineers who

will design the electronic systems of tomorrow Dr Jean Pierre Colinge Taiwan Semiconductor Manufacturing Company TSMC The authors present a creative way to show how different MOS devices can be used for low voltage and low power applications They start with Bulk MOSFET following with SOI MOSFET FinFET gate all around MOSFET Tunnel FET and others It is presented the physics behind the devices models simulations experimental results and applications This book is interesting for researchers graduate and undergraduate students The low energy field is an important topic for integrated circuits in the future and none can stay out of this Prof Joao A Martino University of Sao Paulo Brazil

**Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation** Jose L. Ayala, Braulio Garcia-Camara, Manuel Prieto, Martino Ruggiero, Gilles Sicard, 2011-09-15 This book constitutes the refereed proceedings of the 21st International Conference on Integrated Circuit and System Design PATMOS 2011 held in Madrid Spain in September 2011 The 34 revised full papers presented were carefully reviewed and selected from numerous submissions The paper feature emerging challenges in methodologies and tools for the design of upcoming generations of integrated circuits and systems and focus especially on timing performance and power consumption as well as architectural aspects with particular emphasis on modeling design characterization analysis and optimization

*Ulsi Front-end Technology: Covering From The First Semiconductor Paper To Cmos Finfet Technology* Wai Shing Lau, 2017-08-23 The main focus of this book is ULSI front end technology It covers from the early history of semiconductor science technology from 1874 to state of the art FINFET technology in 2016 Some ULSI back end technology is also covered for example the science and technology of MIM capacitors for analog CMOS has been included in this book

Digital Integrated Circuit Design Using Verilog and Systemverilog Ronald W. Mehler, 2014-09-30 For those with a basic understanding of digital design this book teaches the essential skills to design digital integrated circuits using Verilog and the relevant extensions of SystemVerilog In addition to covering the syntax of Verilog and SystemVerilog the author provides an appreciation of design challenges and solutions for producing working circuits The book covers not only the syntax and limitations of HDL coding but deals extensively with design problems such as partitioning and synchronization helping you to produce designs that are not only logically correct but will actually work when turned into physical circuits Throughout the book many small examples are used to validate concepts and demonstrate how to apply design skills This book takes readers who have already learned the fundamentals of digital design to the point where they can produce working circuits using modern design methodologies It clearly explains what is useful for circuit design and what parts of the languages are only software providing a non theoretical practical guide to robust reliable and optimized hardware design and development Produce working hardware Covers not only syntax but also provides design know how addressing problems such as synchronization and partitioning to produce working solutions Usable examples Numerous small examples throughout the book demonstrate concepts in an easy to grasp manner Essential knowledge Covers the vital design topics of synchronization essential for producing working silicon asynchronous interfacing

techniques and design techniques for circuit optimization including partitioning      Computer Engineering and Technology Weixia Xu, Liquan Xiao, Jinwen Li, Chengyi Zhang, Zhenzhen Zhu, 2016-12-08 This book constitutes the refereed proceedings of the 20th CCF Conference on Computer Engineering and Technology NCCET 2016 held in Xi an China in August 2016 The 21 full papers presented were carefully reviewed and selected from 120 submissions They are organized in topical sections on processor architecture application specific processors computer application and software optimization technology on the horizon      *Electronic Circuit Design* Nihal Kularatna, 2017-12-19 With growing consumer demand for portability and miniaturization in electronics design engineers must concentrate on many additional aspects in their core design The plethora of components that must be considered requires that engineers have a concise understanding of each aspect of the design process in order to prevent bug laden prototypes Electronic Circuit Design allows engineers to understand the total design process and develop prototypes which require little to no debugging before release It provides step by step instruction featuring modern components such as analog and mixed signal blocks in each chapter The book details every aspect of the design process from conceptualization and specification to final implementation and release The text also demonstrates how to utilize device data sheet information and associated application notes to design an electronic system The hybrid nature of electronic system design poses a great challenge to engineers This book equips electronics designers with the practical knowledge and tools needed to develop problem free prototypes that are ready for release      **Timing Performance of Nanometer Digital Circuits Under Process Variations** Victor Champac, Jose Garcia Gervacio, 2018-04-18 This book discusses the digital design of integrated circuits under process variations with a focus on design time solutions The authors describe a step by step methodology going from logic gates to logic paths to the circuit level Topics are presented in comprehensively without overwhelming use of analytical formulations Emphasis is placed on providing digital designers with understanding of the sources of process variations their impact on circuit performance and tools for improving their designs to comply with product specifications Various circuit level design hints are highlighted so that readers can use them to improve their designs A special treatment is devoted to unique design issues and the impact of process variations on the performance of FinFET based circuits This book enables readers to make optimal decisions at design time toward more efficient circuits with better yield and higher reliability      Intelligent Computing Kohei Arai, 2023-08-19 This book is a collection of extremely well articulated insightful and unique state of the art papers presented at the Computing Conference which took place in London on June 22 23 2023 A total of 539 papers were received out of which 193 were selected for presenting after double blind peer review The book covers a wide range of scientific topics including IoT Artificial Intelligence Computing Data Science Networking Data security and Privacy etc The conference was successful in reaping the advantages of both online and offline modes The goal of this conference is to give a platform to researchers with fundamental contributions and to be a premier venue for academic and industry practitioners to share new ideas and development

experiences We hope that readers find this book interesting and valuable We also expect that the conference and its publications will be a trigger for further related research and technology improvements in this important subject

**Low Power Vlsi Design And Technology** Farid N Najm,Garey K-h Yeap,1996-08-30 Low power and low energy VLSI has become an important issue in today s consumer electronics This book is a collection of pioneering applied research papers in low power VLSI design and technology A comprehensive introductory chapter presents the current status of the industry and academic research in the area of low power VLSI design and technology Other topics cover logic synthesis floorplanning circuit design and analysis from the perspective of low power requirements The readers will have a sampling of some key problems in this area as the low power solutions span the entire spectrum of the design process The book also provides excellent references on up to date research and development issues with practical solution techniques

**Micro Electronic Circuit Design for High Performance Applications** Dr. S.Sathya,Dr. Priyanka Veeramosu,Dr. R. Boopathi,Dr. Bindu K V,Mr. Nishant S,2025-01-28 Microelectronic Circuit Design for High Performance Applications is a comprehensive that explores advanced circuit design principles tailored for high speed low power and efficient electronic systems Topics such as semiconductor devices analog and digital circuit design signal integrity and power management the book provides in depth insights into optimizing performance in modern electronic applications It integrates theoretical foundations with practical design methodologies making it valuable for engineers researchers and students involved in cutting edge microelectronics With a focus on emerging technologies the addresses challenges in miniaturization integration and high frequency operation ensuring relevance in contemporary and future electronic design

**The Art and Science of Microelectronic Circuit Design** Anatoly Belous,Vitali Saladukha,2022-02-10 This book guides readers through the entire complex of interrelated theoretical and practical aspects of the end to end design and organization of production of silicon submicron integrated circuits The discussion includes the theoretical foundations of the operation of field effect and bipolar transistors the methods and peculiarities of the structural and schematic design basic circuit design and system design engineering solutions for bipolar CMOS BiCMOS and TTL integrated circuits standard design libraries and typical design flows

**Wafer Level 3-D ICs Process Technology** Chuan Seng Tan,Ronald J. Gutmann,L. Rafael Reif,2009-06-29 Three dimensional 3D integration is clearly the simplest answer to most of the semiconductor industry s vexing problems heterogeneous integration and reductions of power form factor delay and even cost Conceptually the power latency and form factor of a system with a fixed number of transistors all scale roughly linearly with the diameter of the smallest sphere enclosing frequently interacting devices This clearly provides the fundamental motivation behind 3D technologies which vertically stack several strata of device and interconnect layers with high vertical interconnectivity In addition the ability to vertically stack strata with divergent and even incompatible process flows provides for low cost and low parasitic integration of diverse technologies such as sensors energy scavengers nonvolatile memory dense memory fast memory processors and RF layers These capabilities



coupled with today's trends of increasing levels of integrated functionality, lower power, smaller form factor, increasingly divergent process flows, and functional diversification would seem to make 3D technologies a natural choice for most of the semiconductor industry. Since the concept of vertical integration of different strata has been around for over 20 years, why aren't vertically stacked strata endemic to the semiconductor industry? The simple answer to this question is that in the past the 3D advantages, while interesting, were not necessary due to the tremendous opportunities offered by geometric scaling. In addition, even when the global interconnect problem of high performance single core processors seemed insurmountable, without innovations such as 3D alternative architectural solutions such as multicores could effectively delay but not eliminate the need for 3D.

**Low Power Design Methodologies** Jan M. Rabaey, Massoud Pedram, 2012-12-06  
Low Power Design Methodologies presents the first in depth coverage of all the layers of the design hierarchy ranging from the technology, circuit, logic, and architectural levels up to the system layer. The book gives insight into the mechanisms of power dissipation in digital circuits and presents state of the art approaches to power reduction. Finally, it introduces a global view of low power design methodologies and how these are being captured in the latest design automation environments. The individual chapters are written by the leading researchers in the area, drawn from both industry and academia. Extensive references are included at the end of each chapter.  
Audience: A broad introduction for anyone interested in low power design. Can also be used as a text book for an advanced graduate class. A starting point for any aspiring researcher.

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