Sumit Ahuja · Avinash Lakshminarayana Sandeep Kumar Shukla

Low Power Design with High-Level Power Estimation and Power-Aware Synthesis



# **Low Power Design With High Level Power Estimation And Power Aware Synthesis**

Saraju P. Mohanty, Nagarajan Ranganathan, Elias Kougianos, Priyardarsan Patra Low Power Design With High Level Power Estimation And Power Aware Synthesis:

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